

FIG. 1

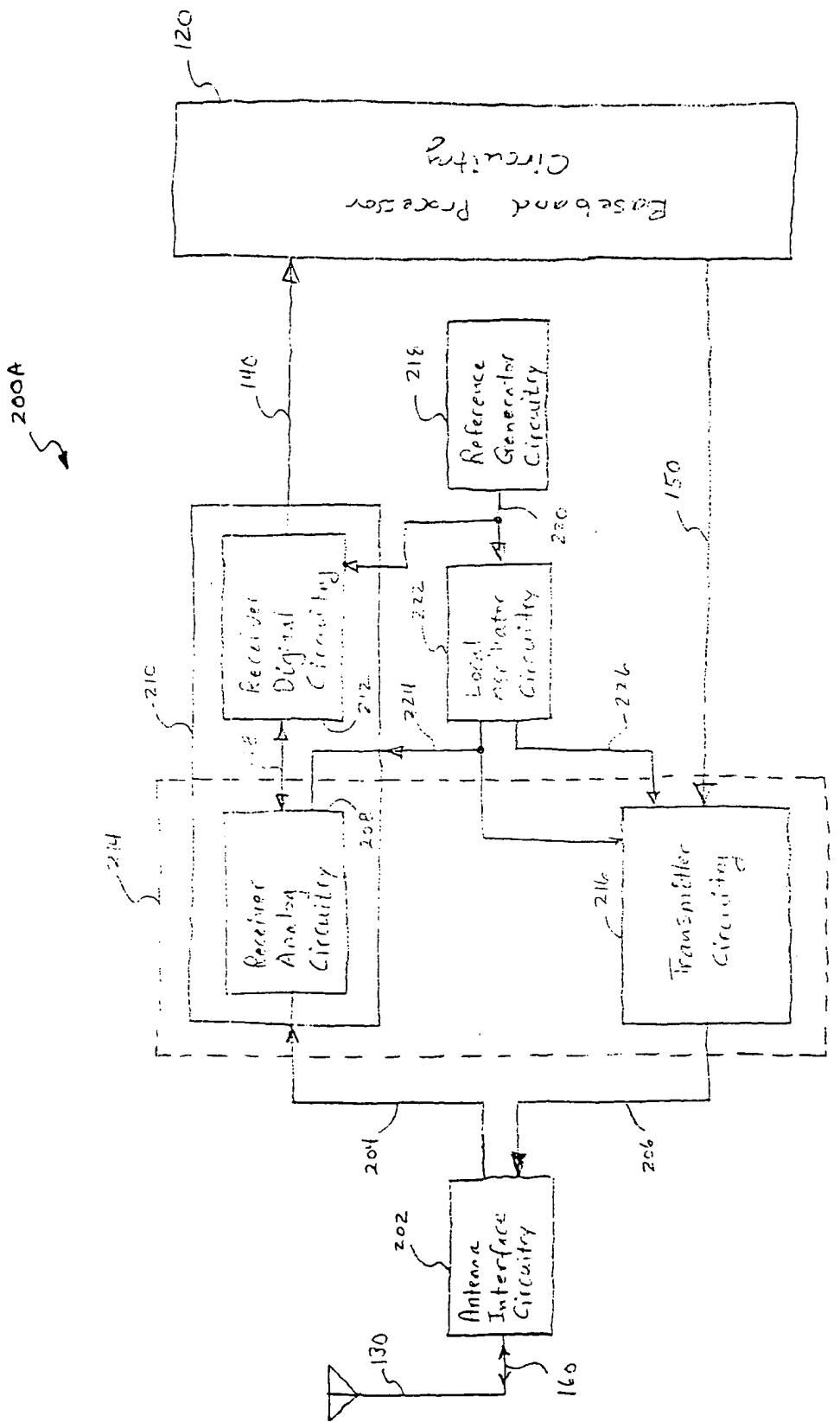


FIG. 2A

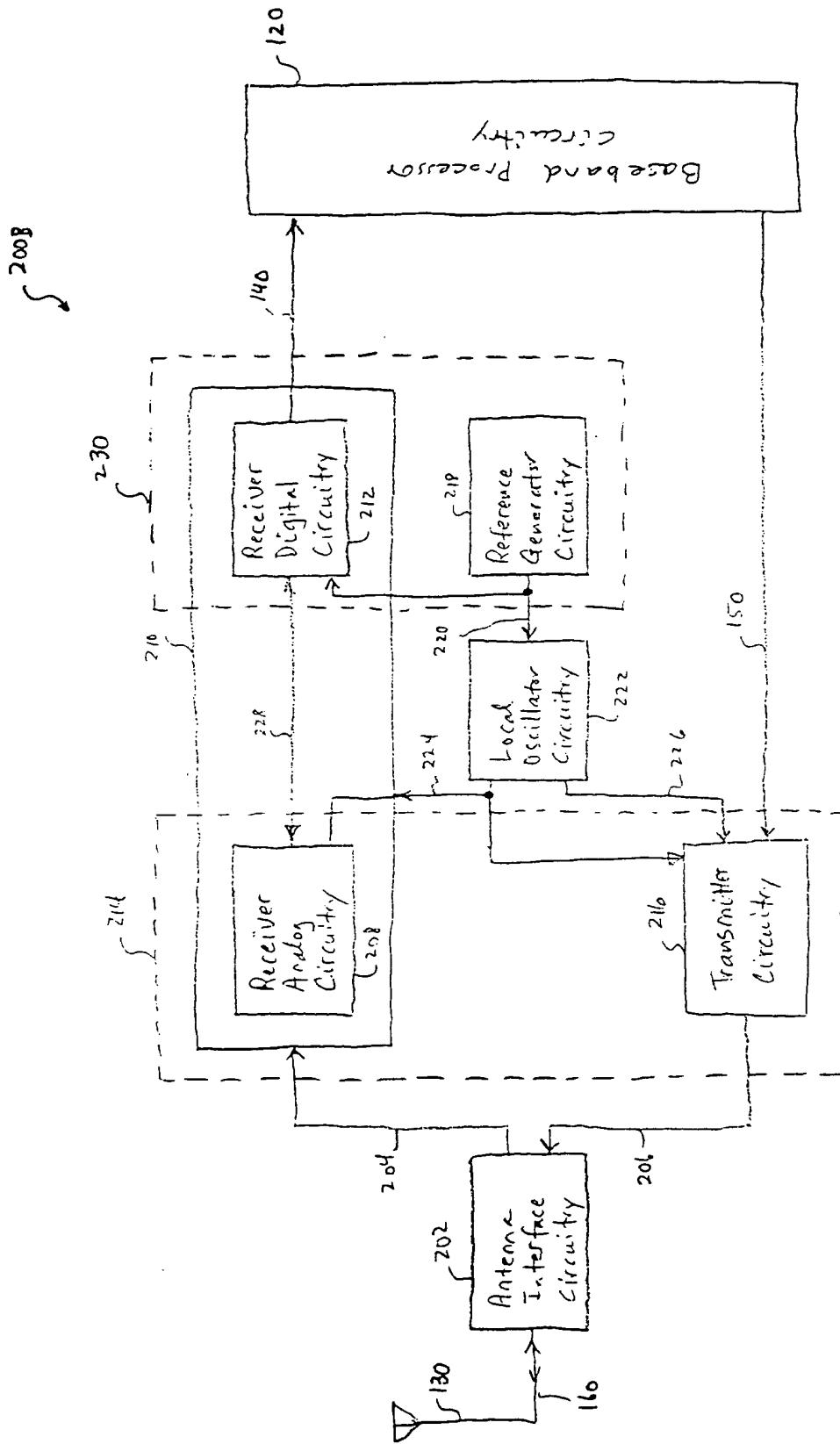


FIG. 2B

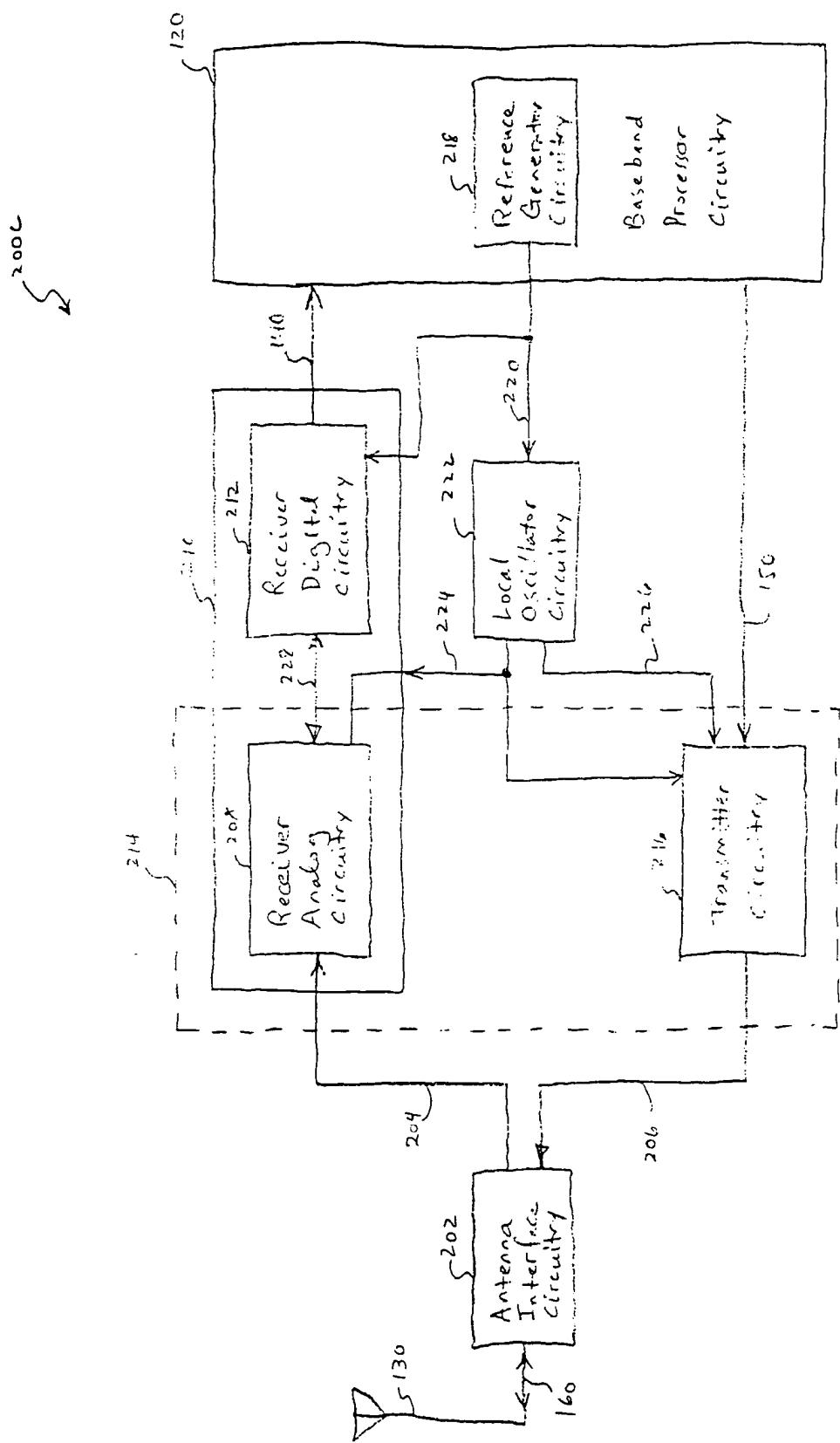


FIG. 2C

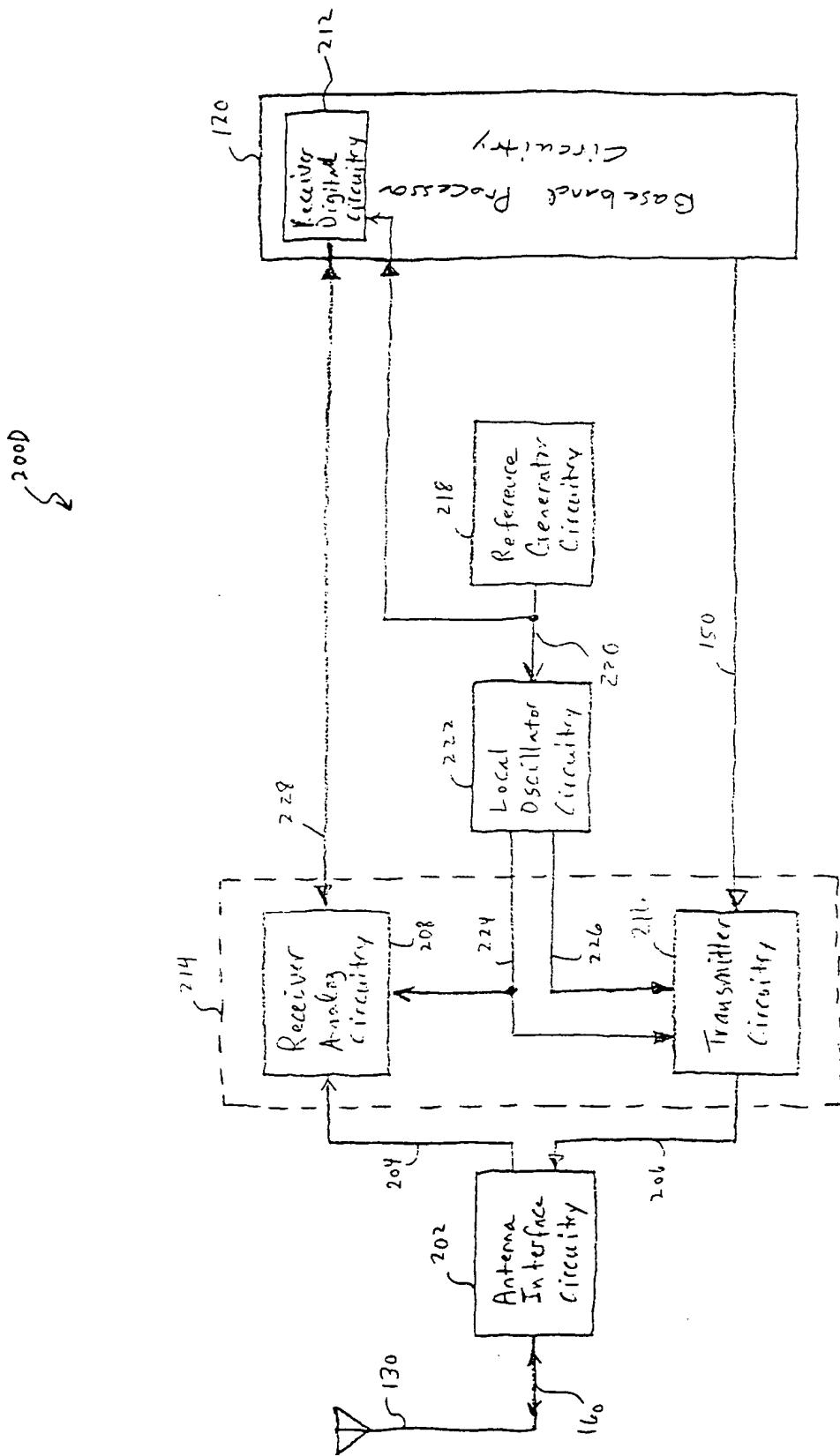


FIG. 2 D

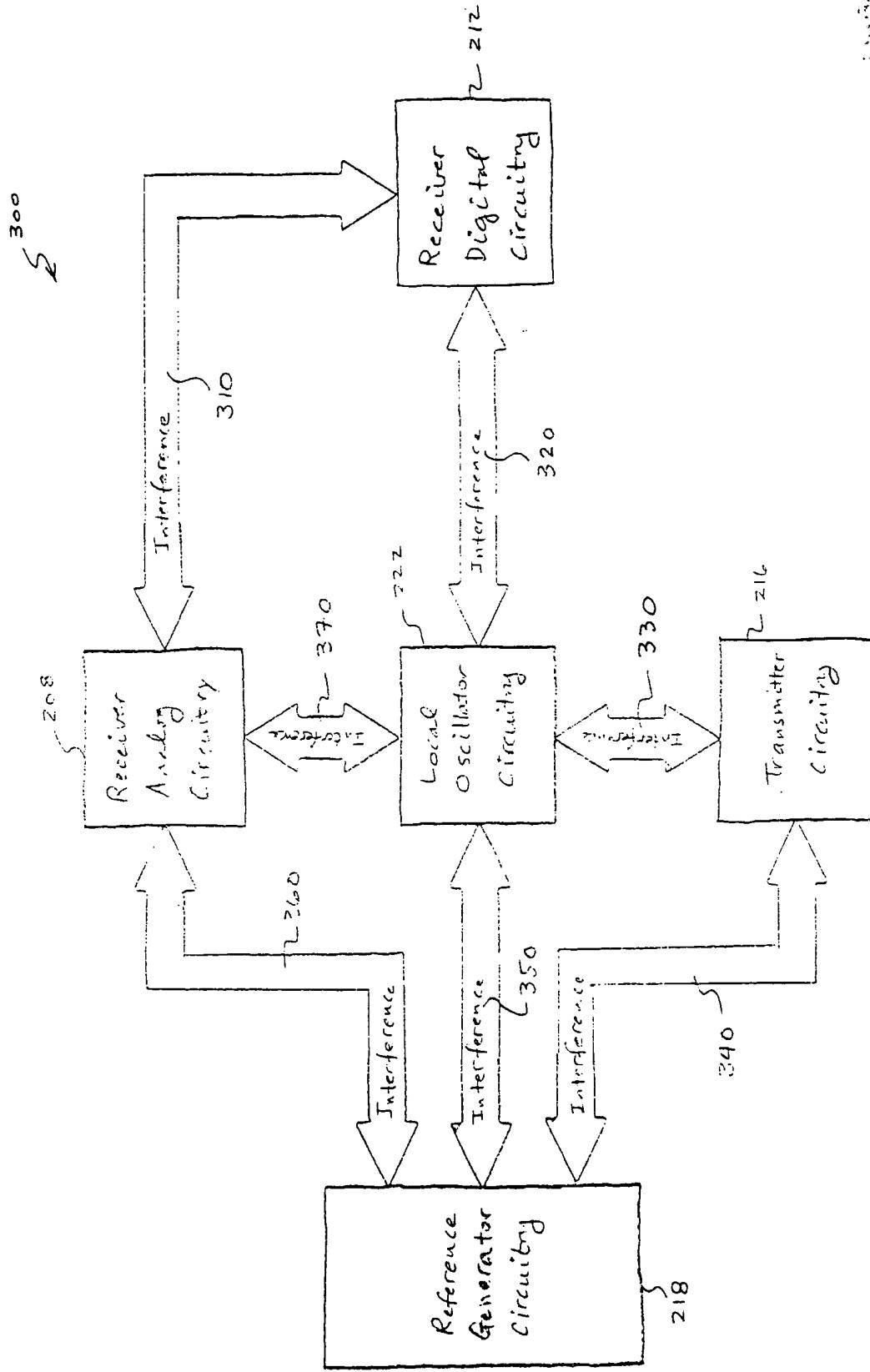


FIG. 3

400

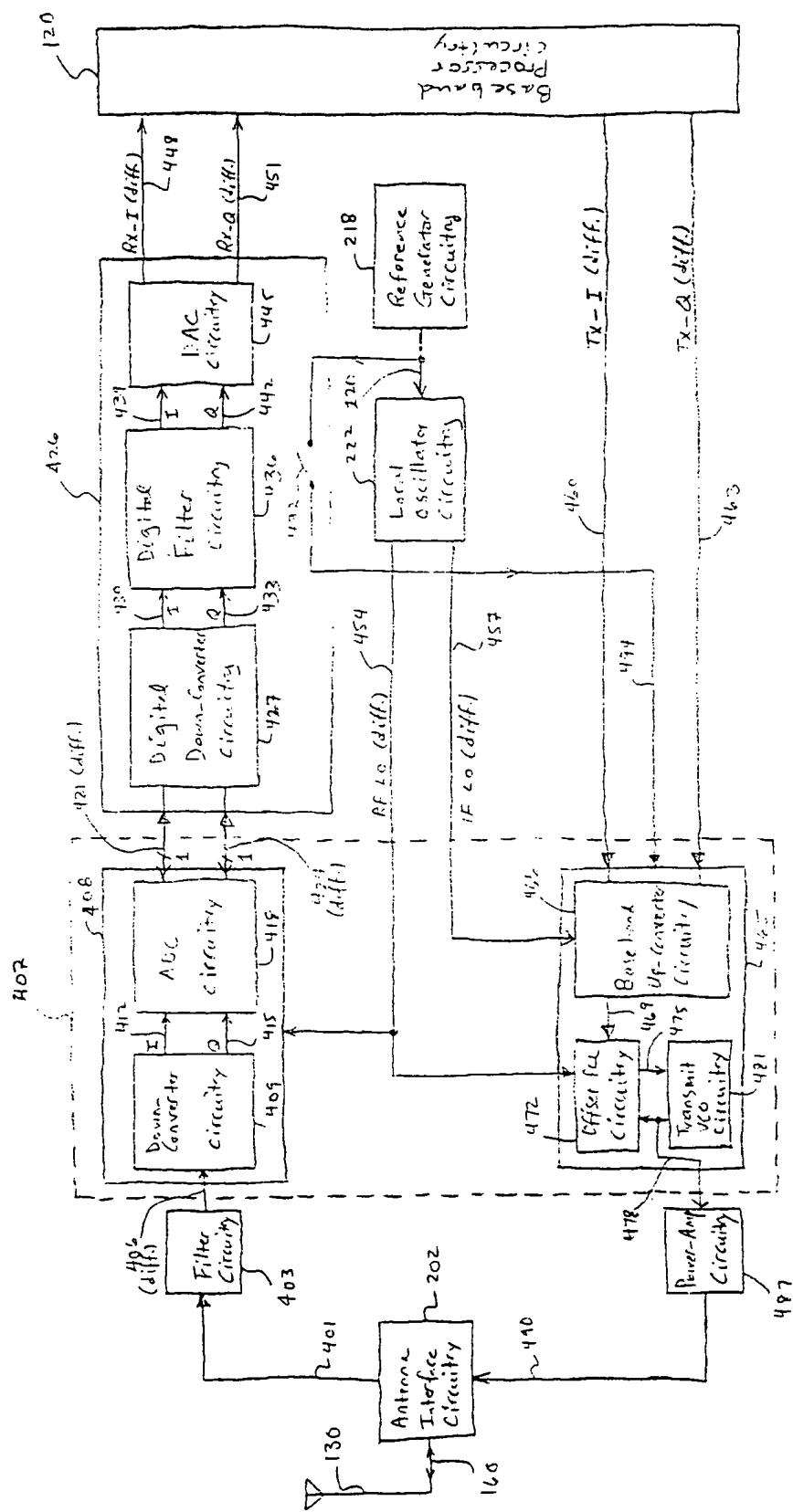


FIG. 4

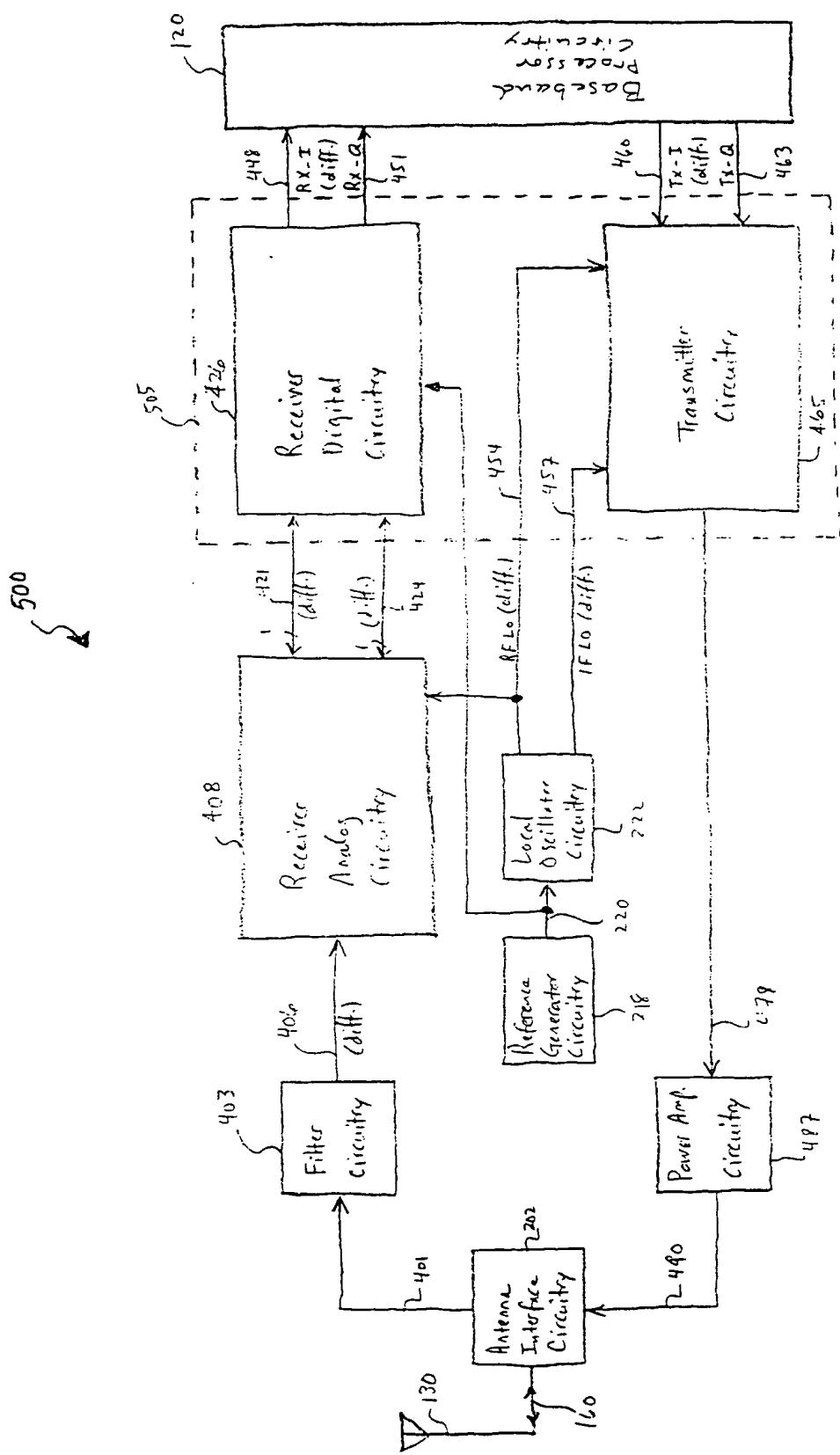


FIG. 5

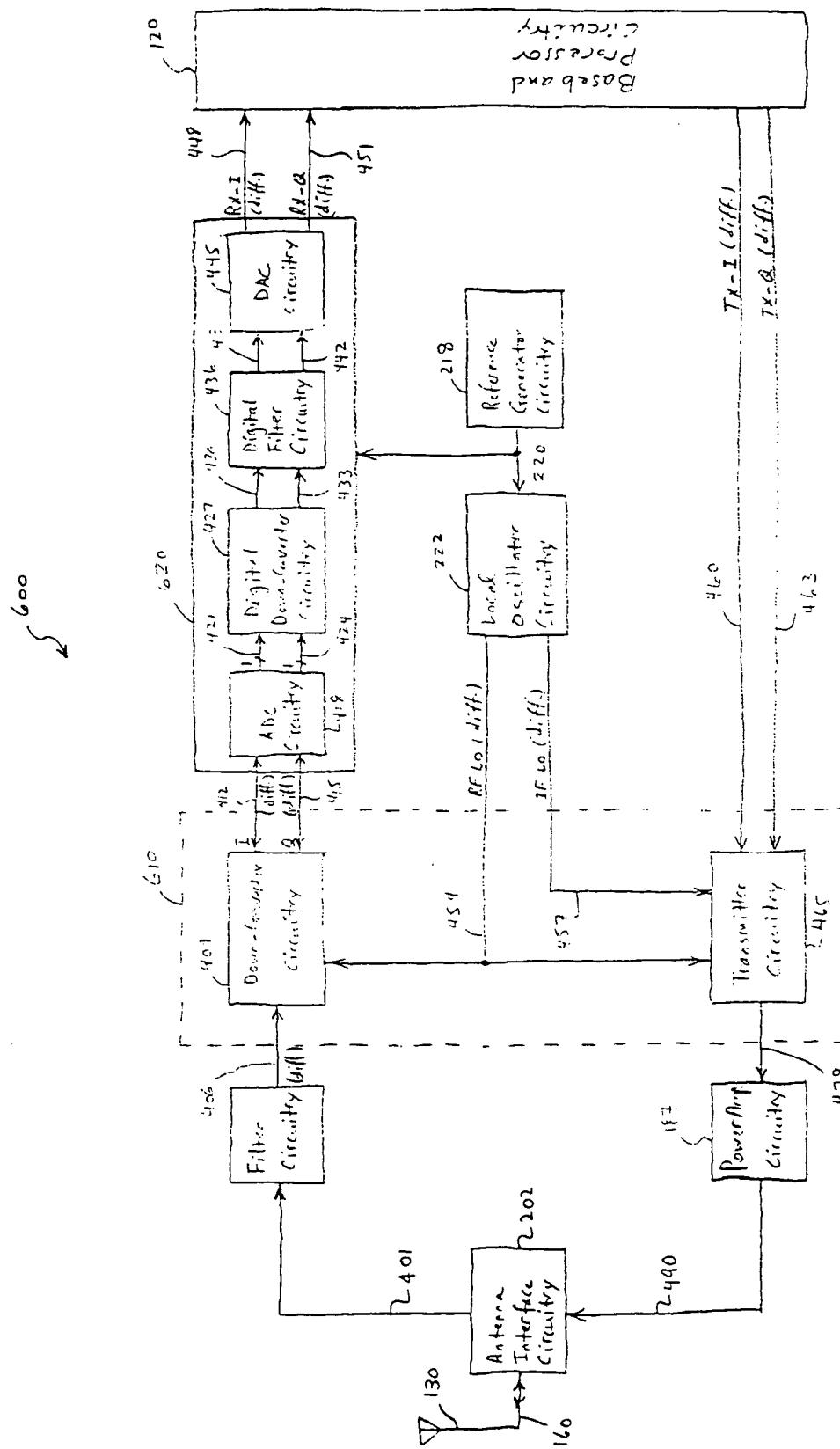


FIG. 6

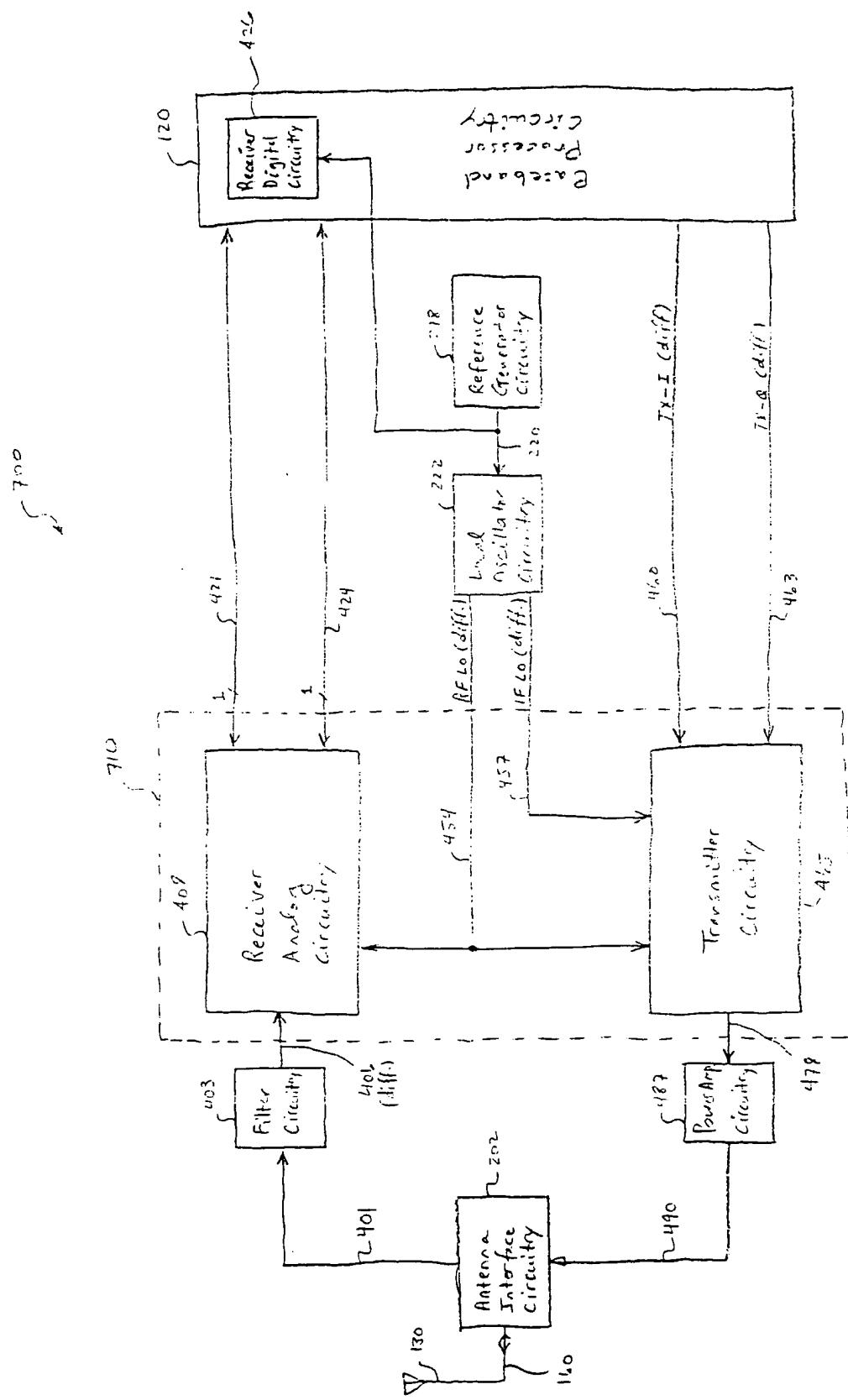


FIG. 7

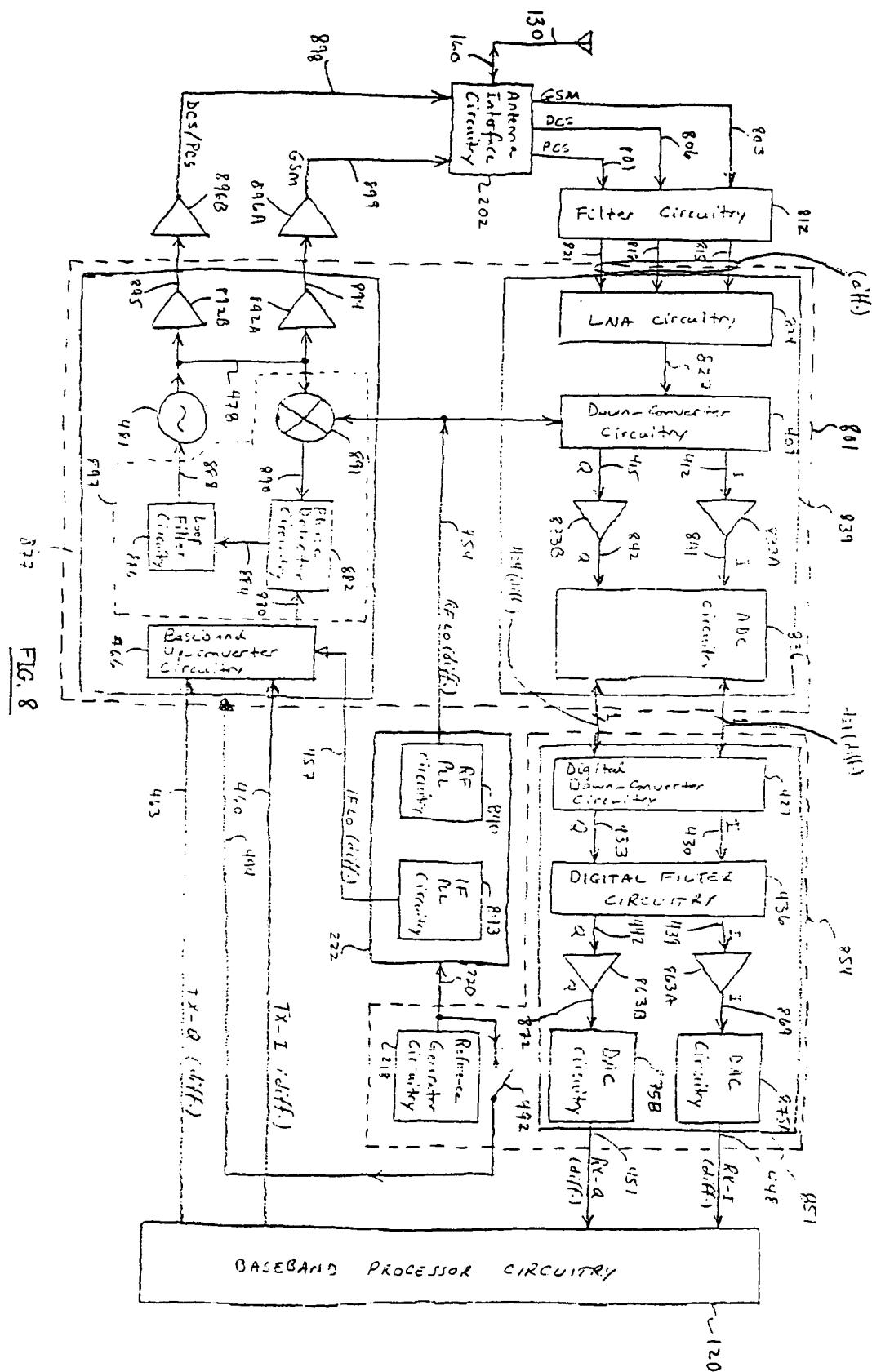


FIG. 8

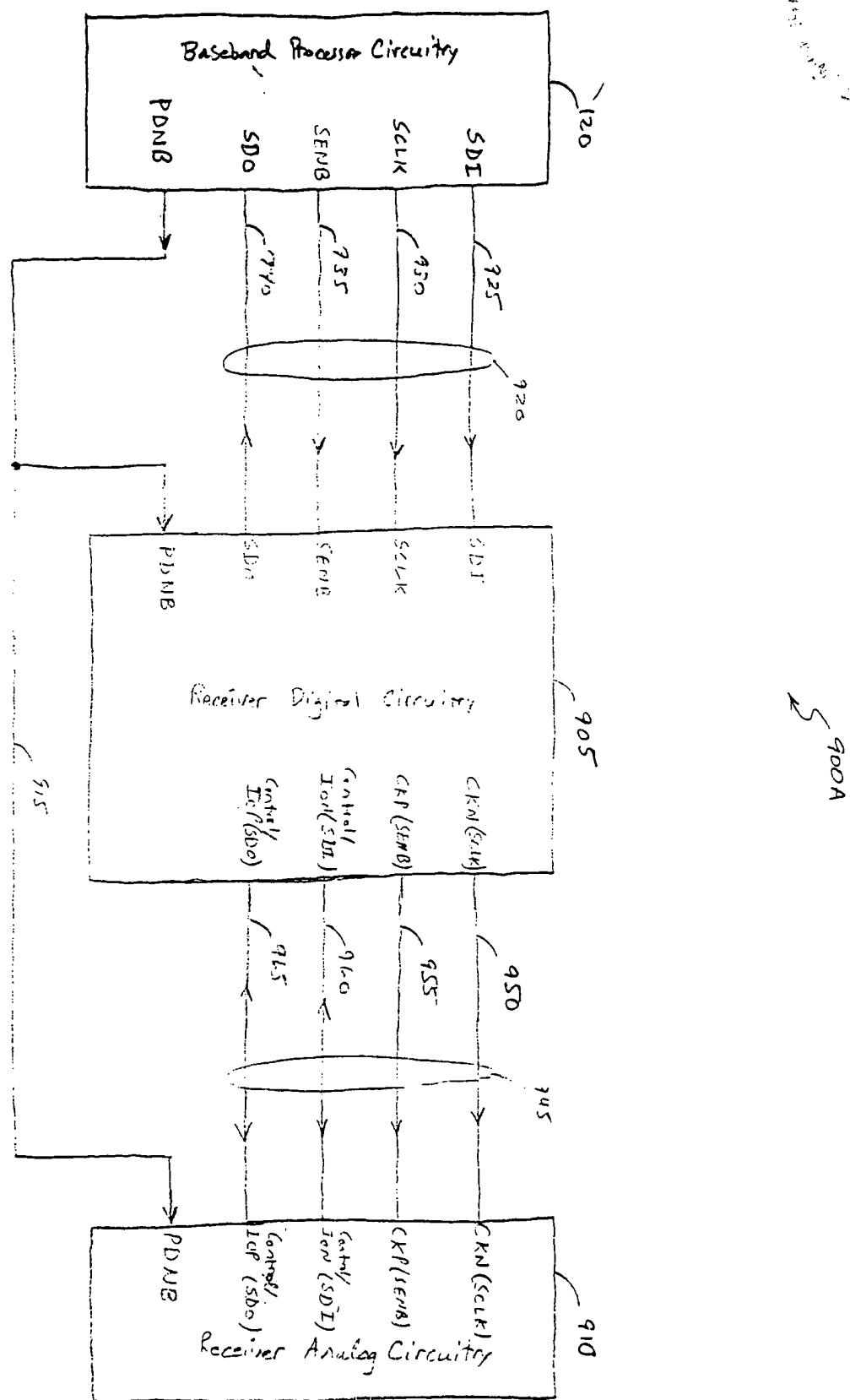


FIG. 9A

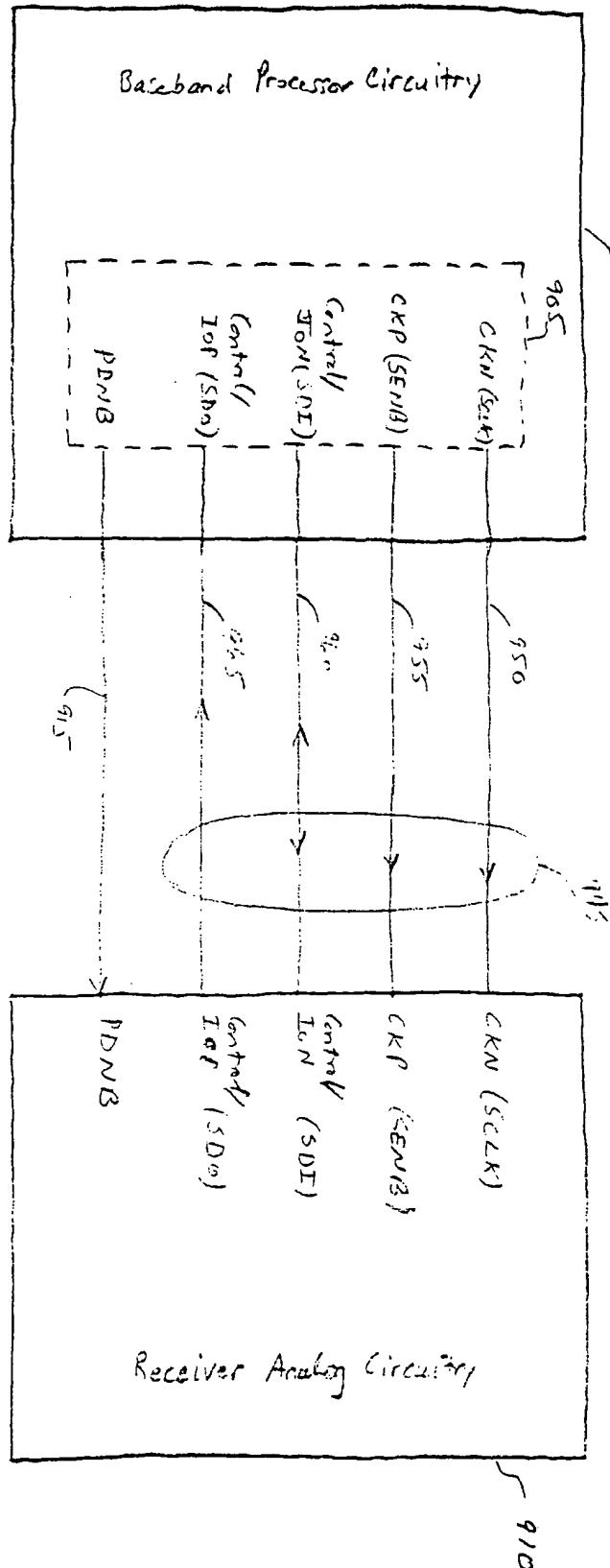


FIG. 9B

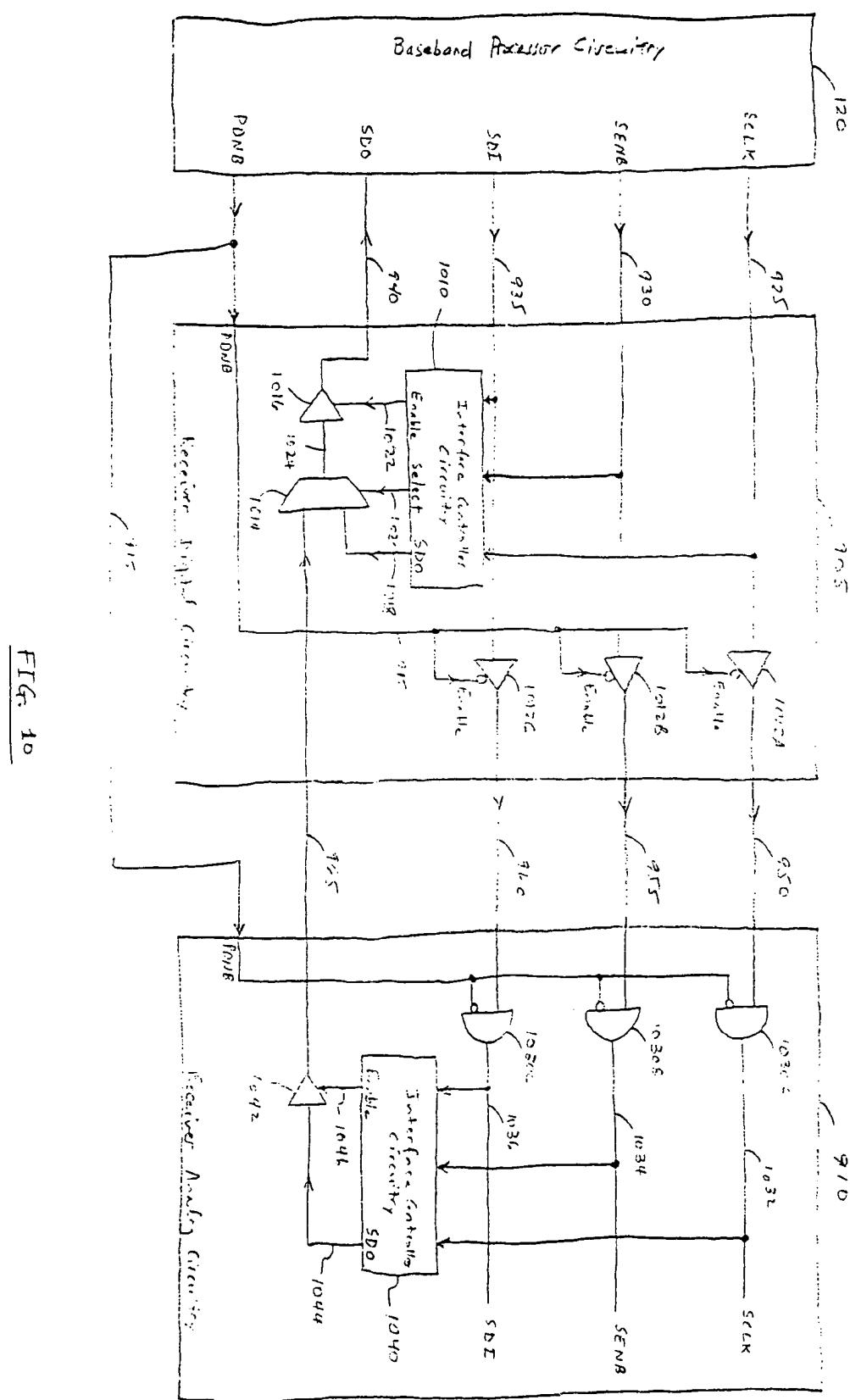


FIG. 10

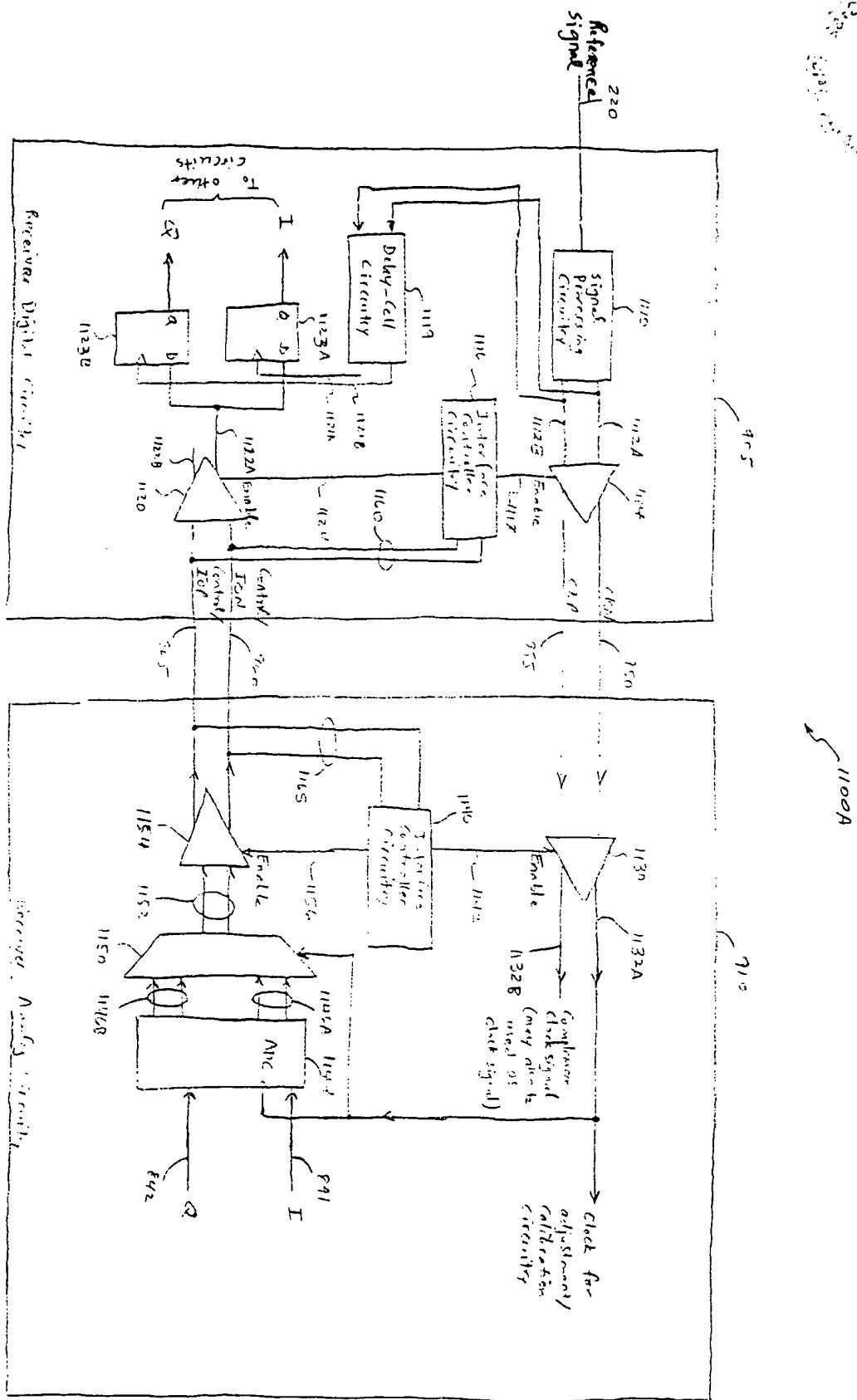


FIG. 11A

Reference Digital circuit

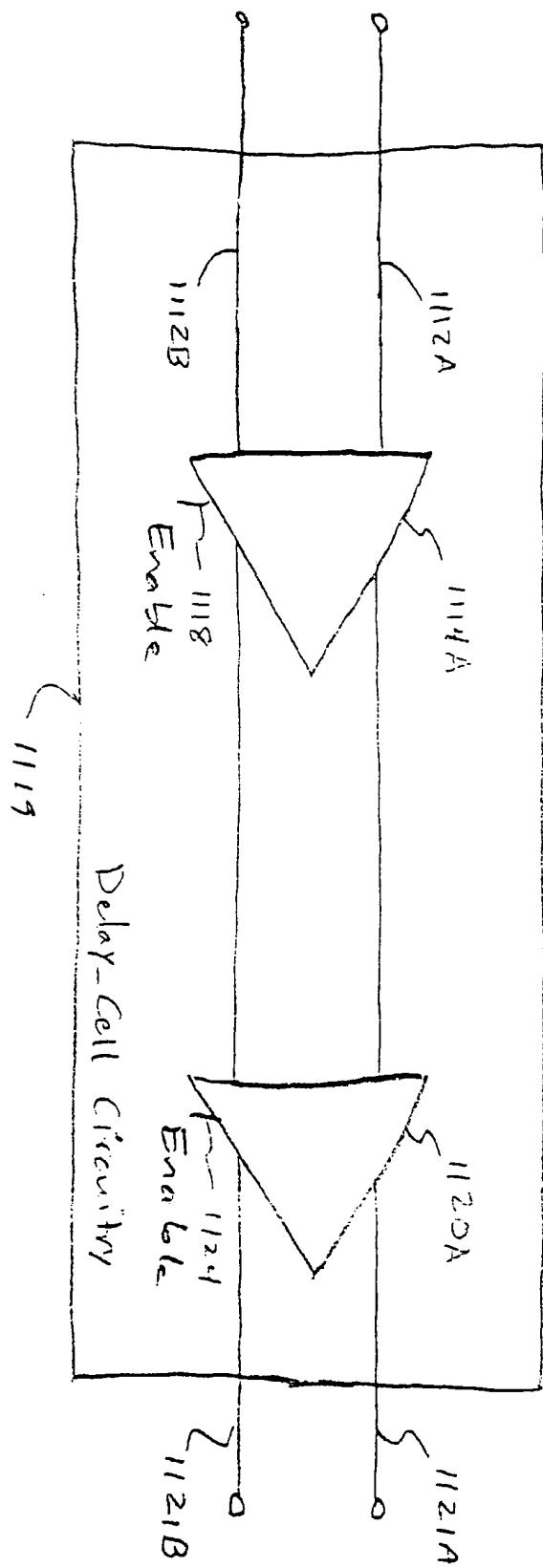


FIG. 11B

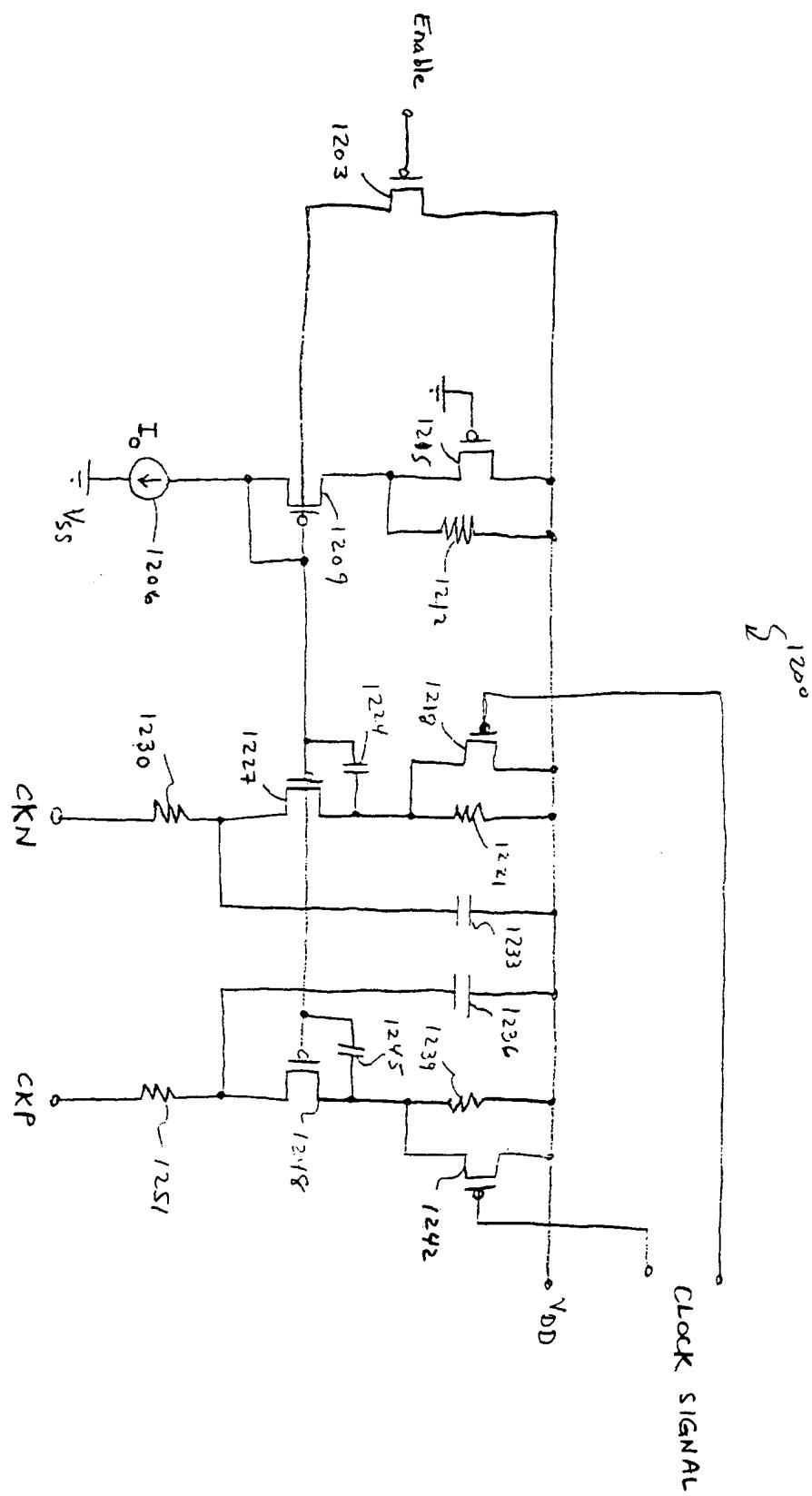


FIG. 12

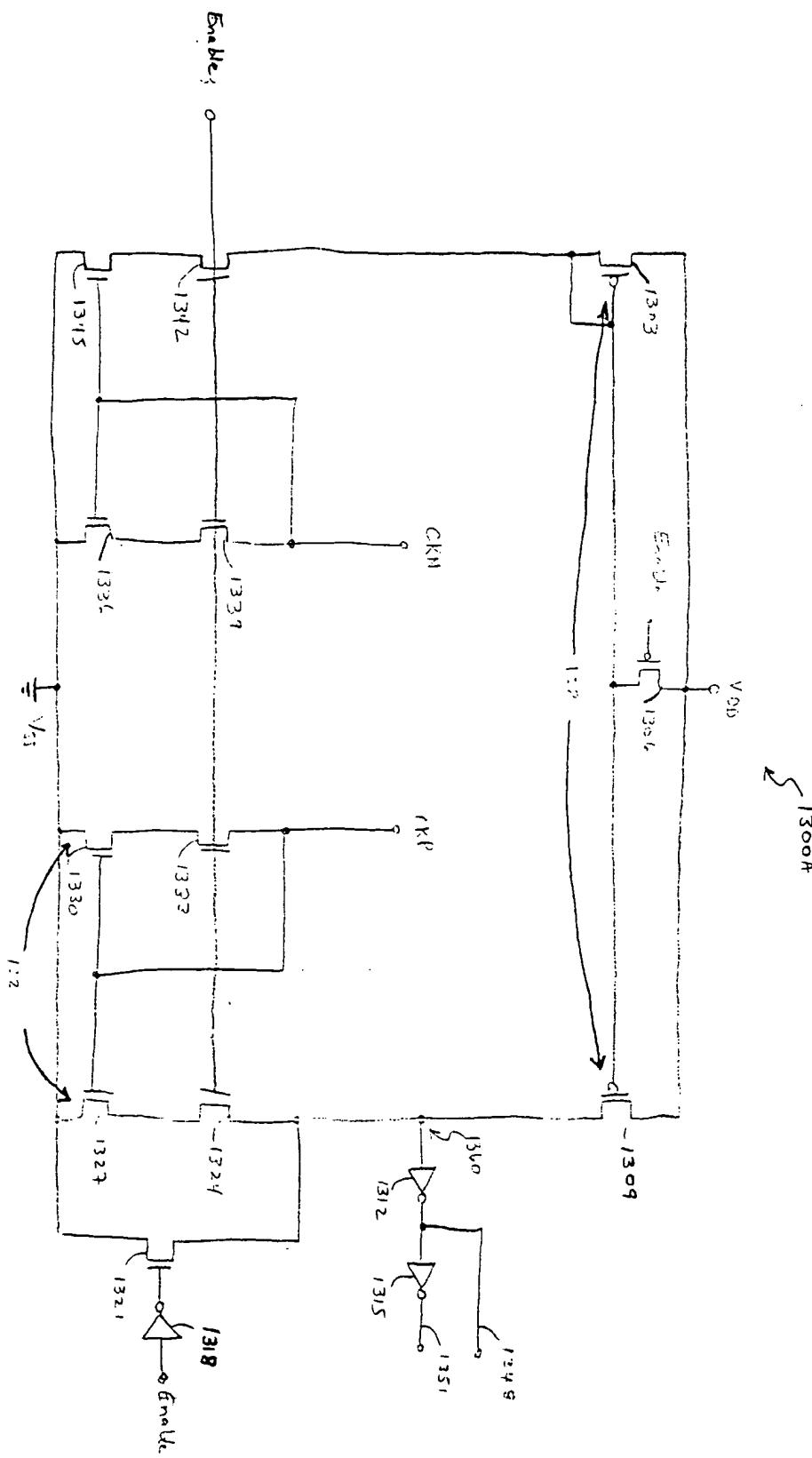


FIG. 13A

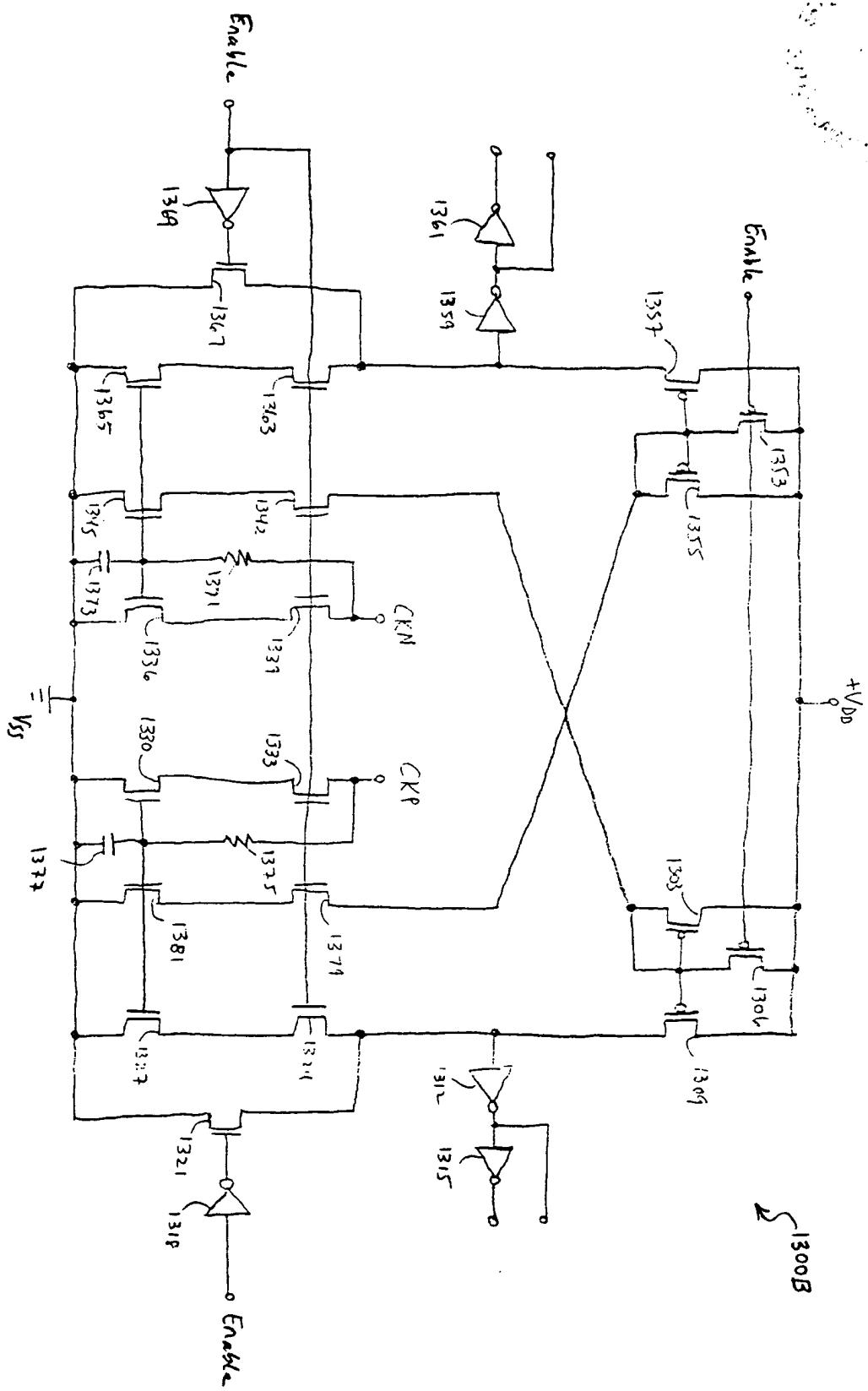
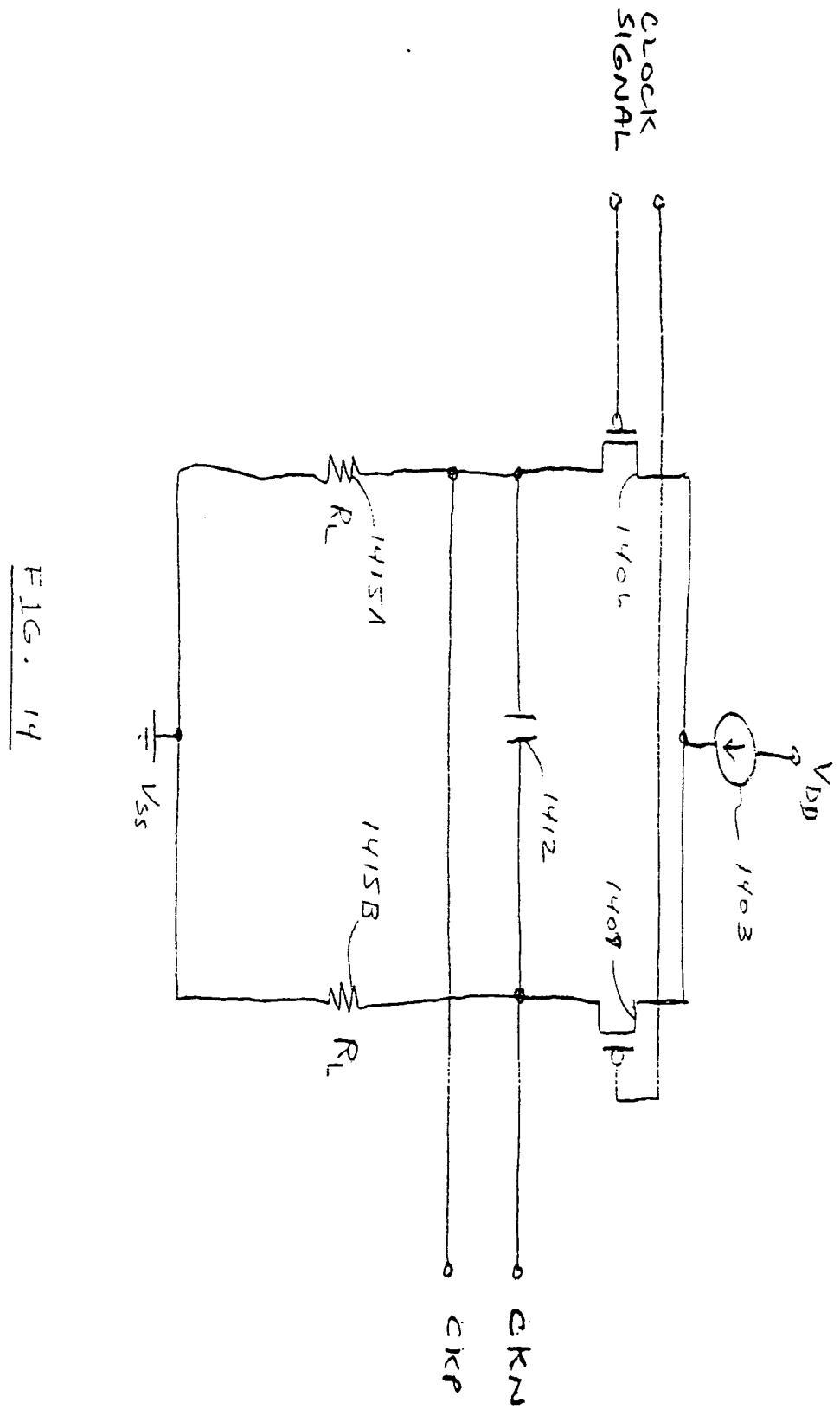


FIG. 13B



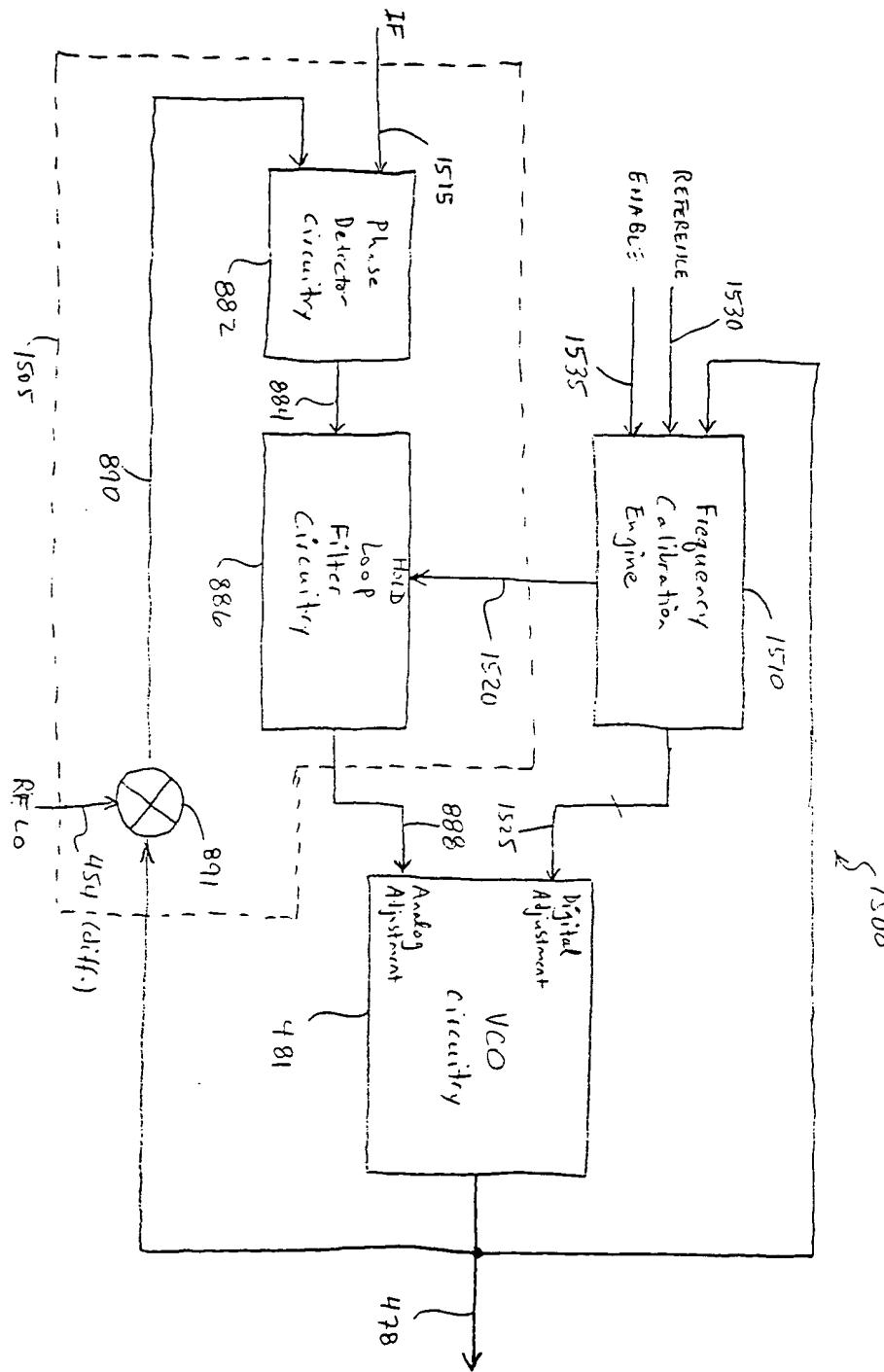


Fig. 15

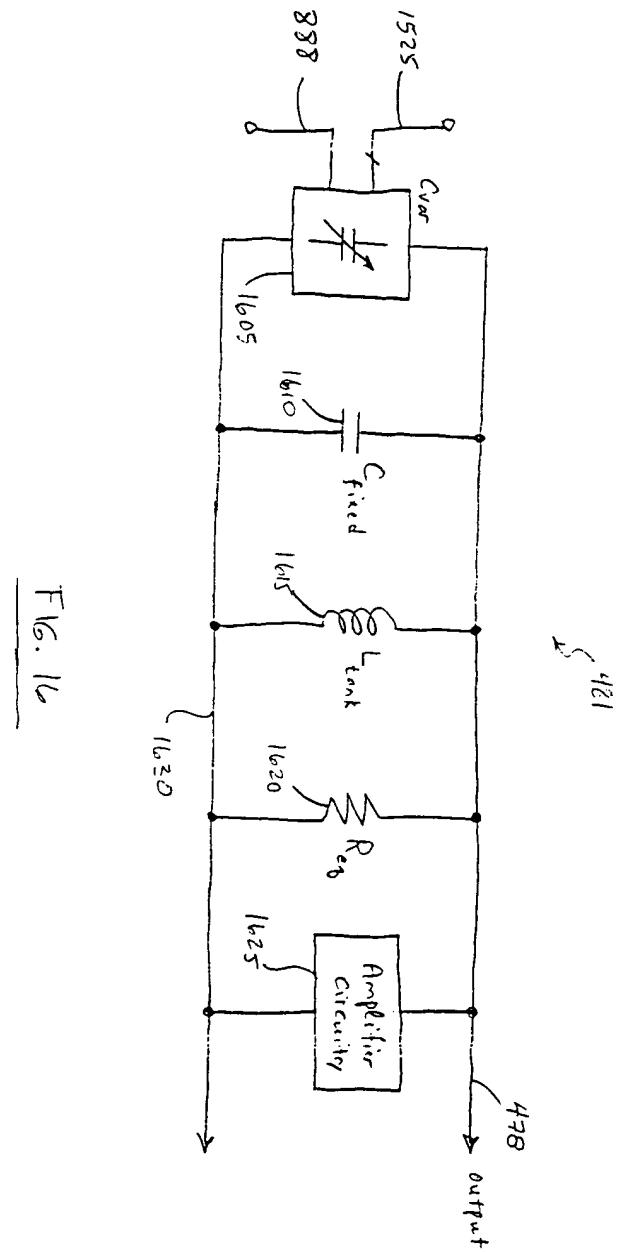


FIG. 16

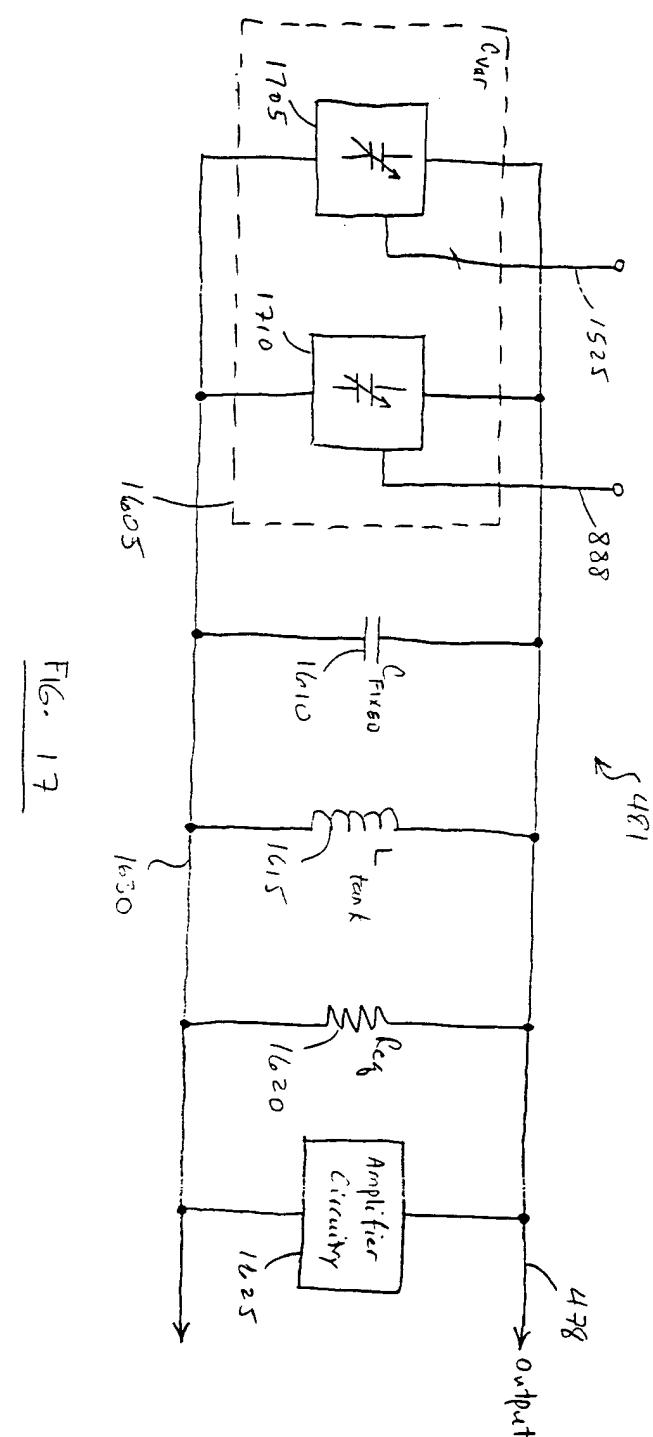


FIG. 17

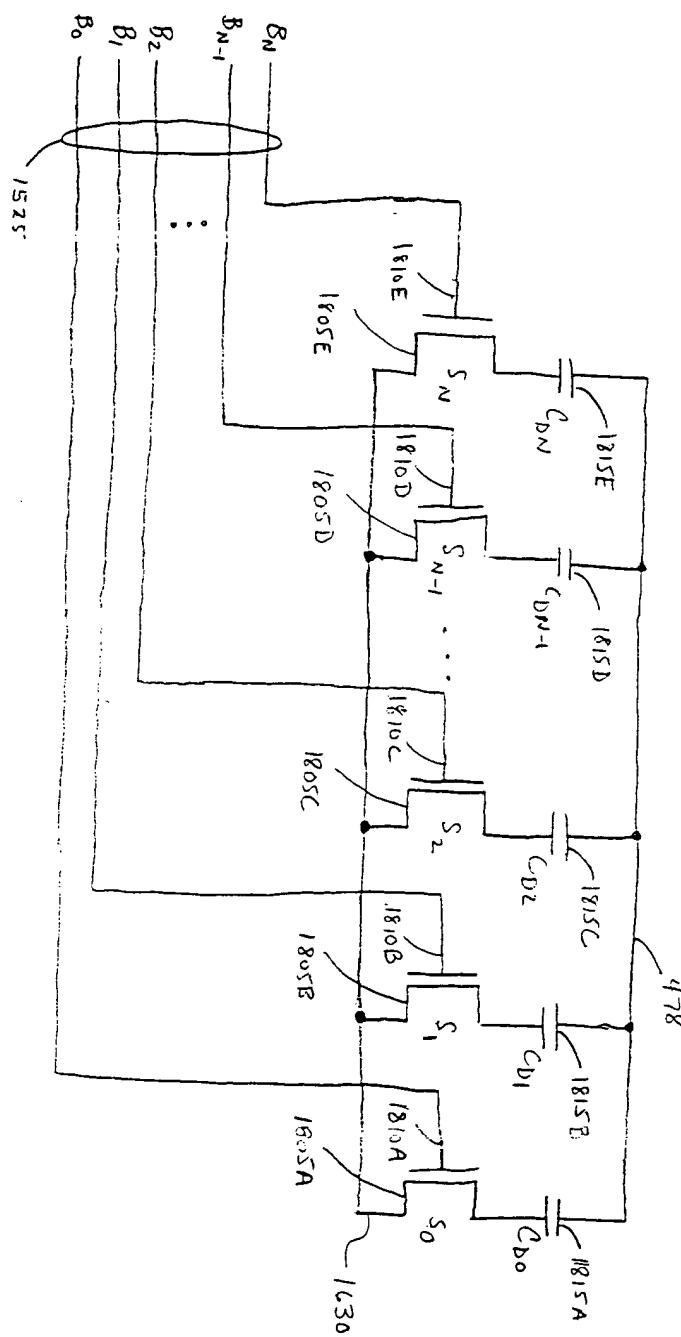


FIG. 18

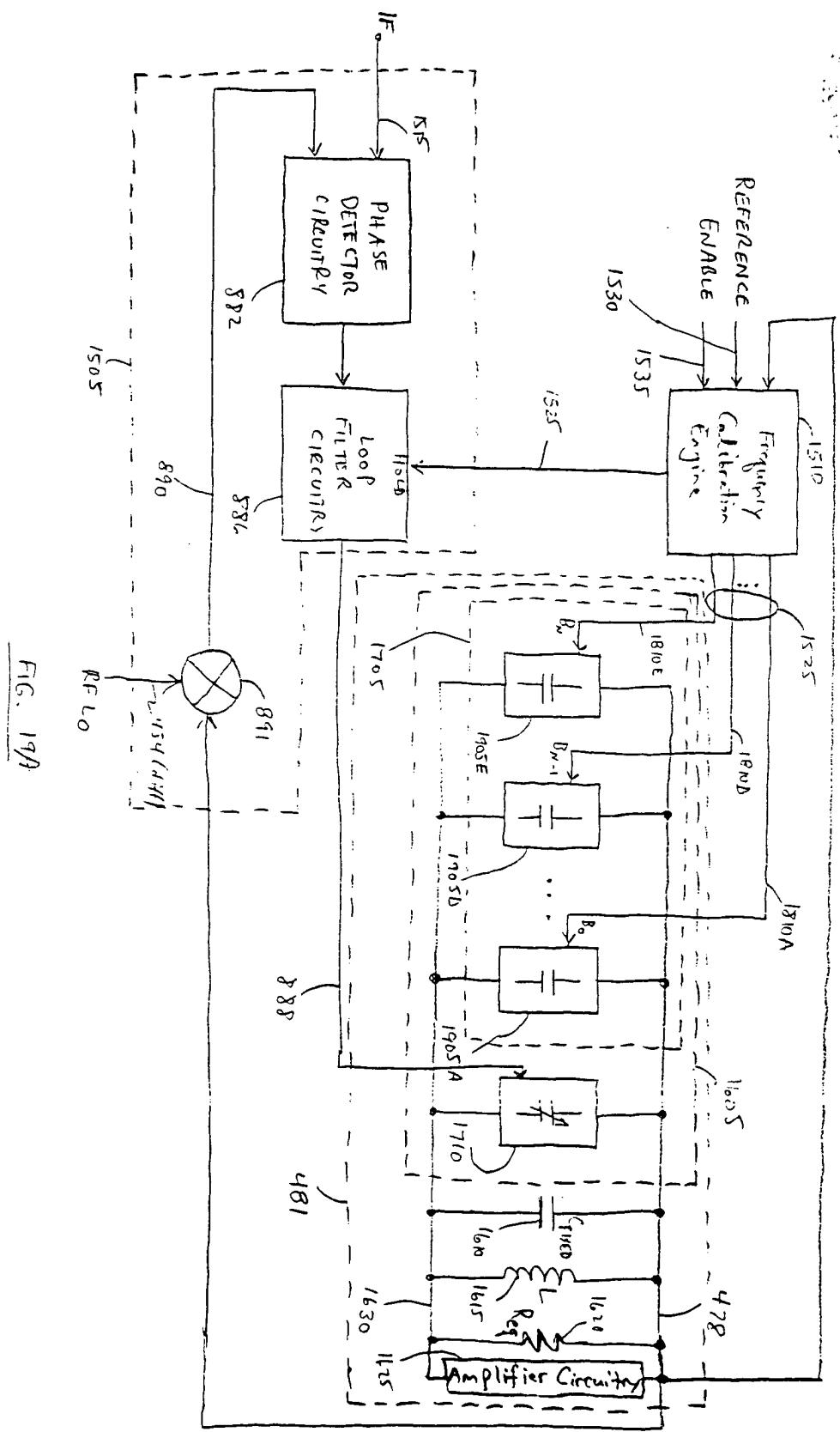


FIG. 199A

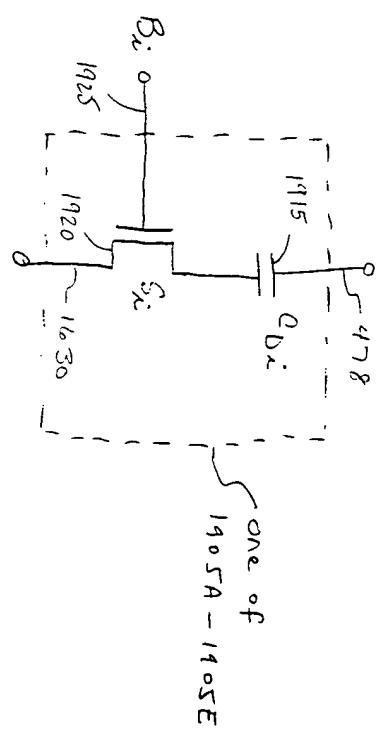


Fig. 19B

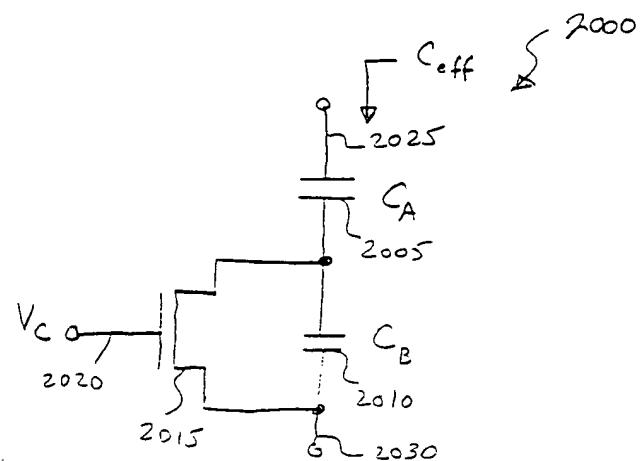


FIG. 20

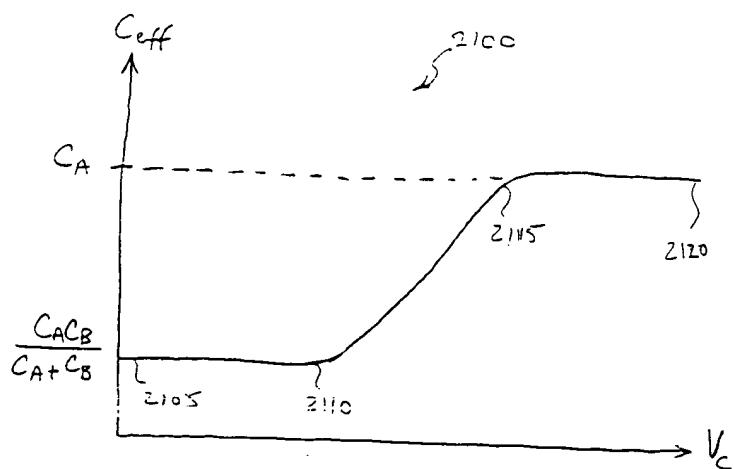


FIG. 21

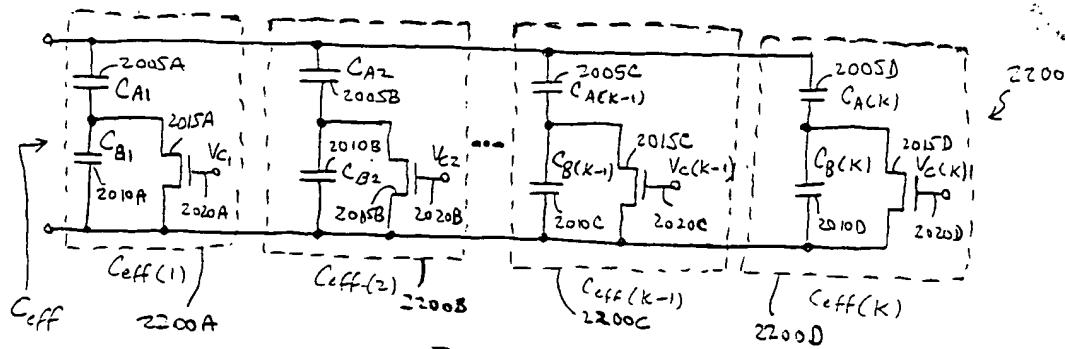


FIG. 22

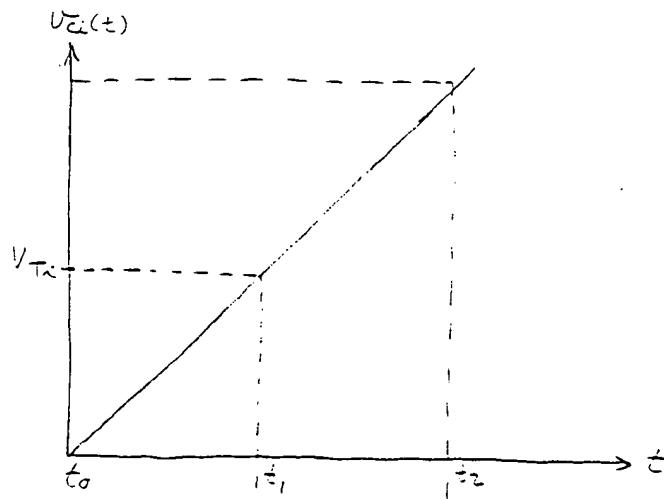


FIG. 23A

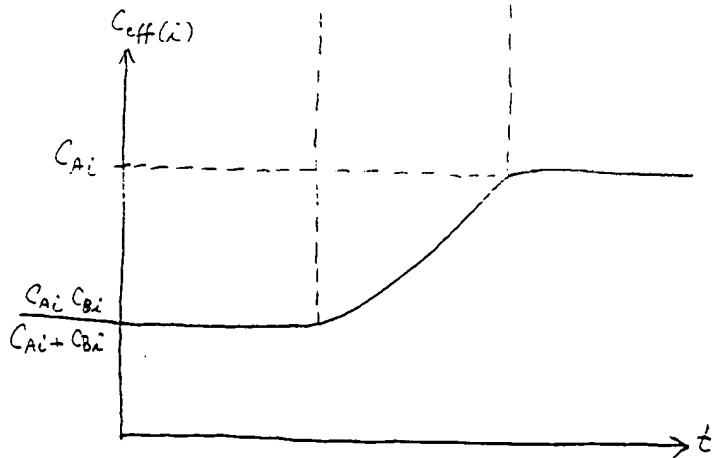


FIG. 23B

FIG. 24A

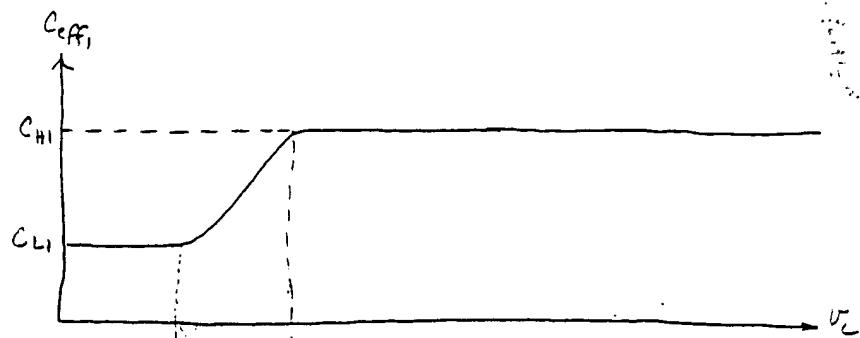


FIG. 24B

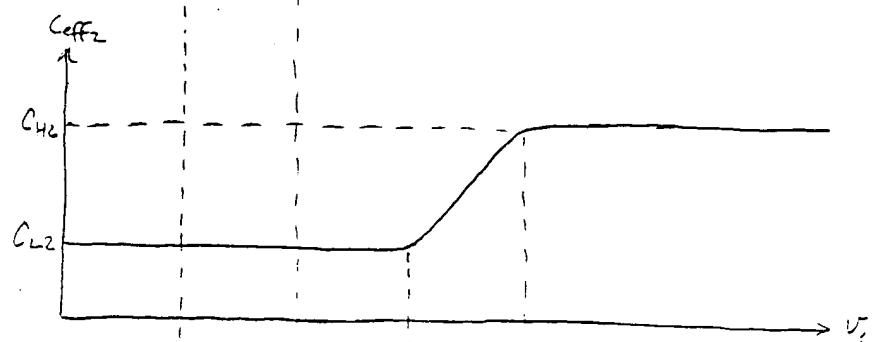


FIG. 24C

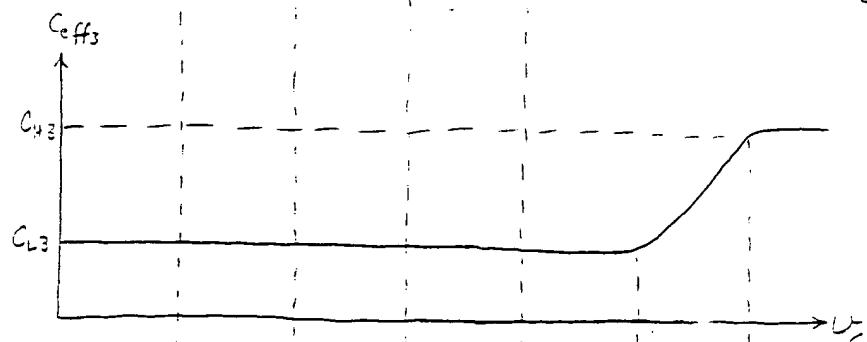
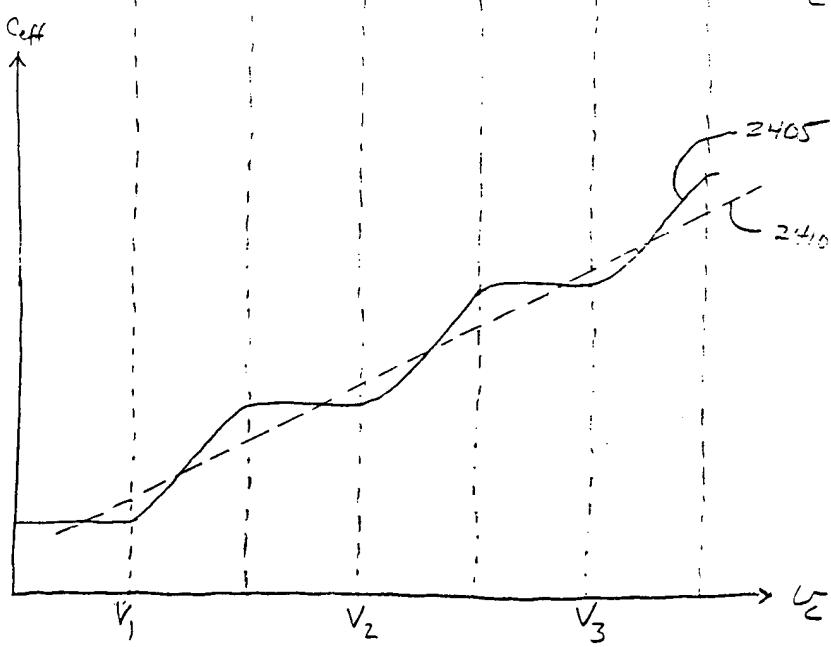


FIG. 24D



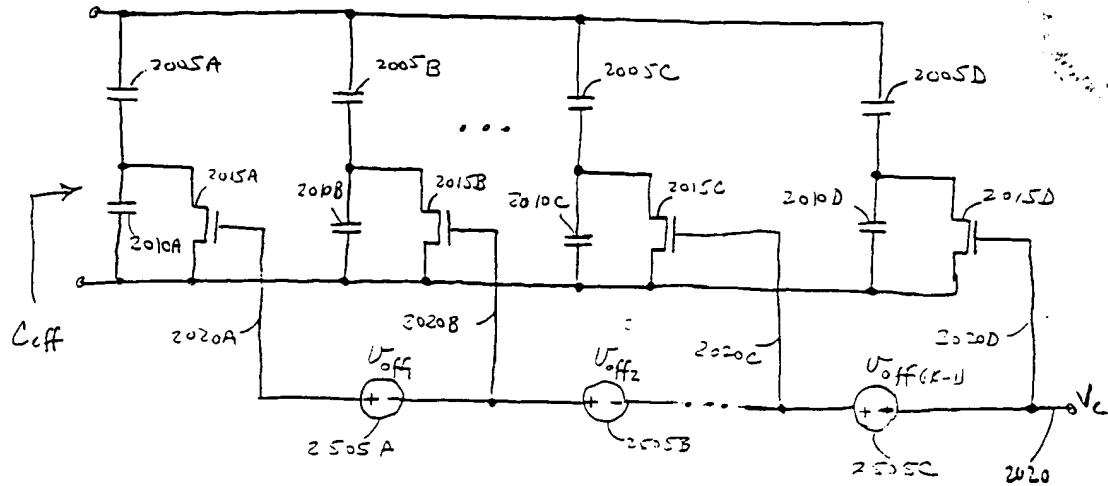


FIG. 25

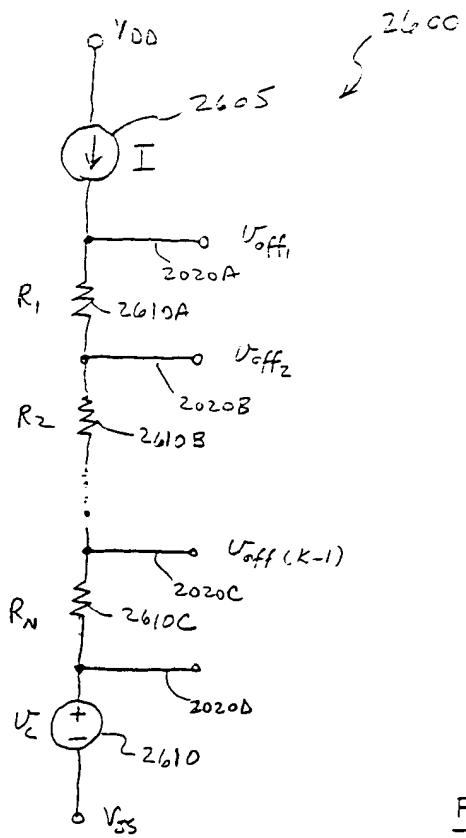


FIG. 26

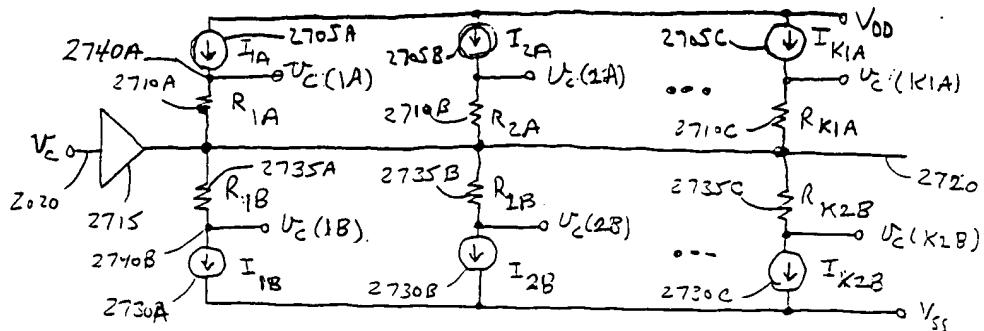


FIG. 27

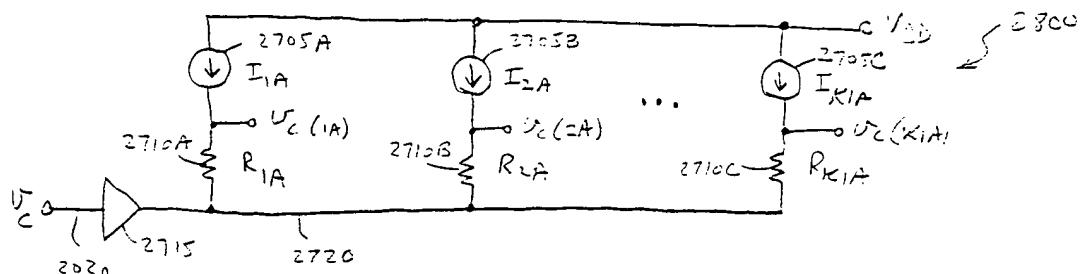


FIG. 28

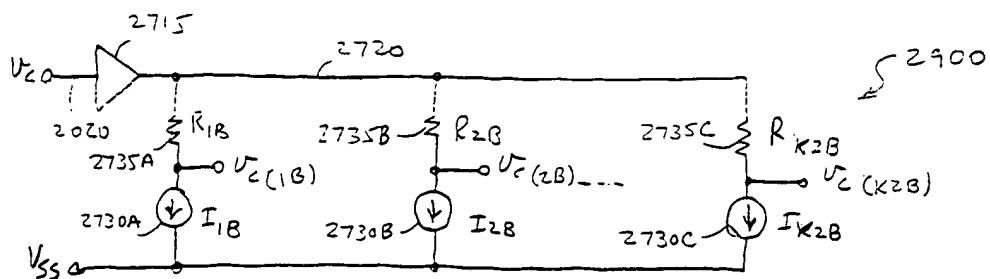


FIG. 29

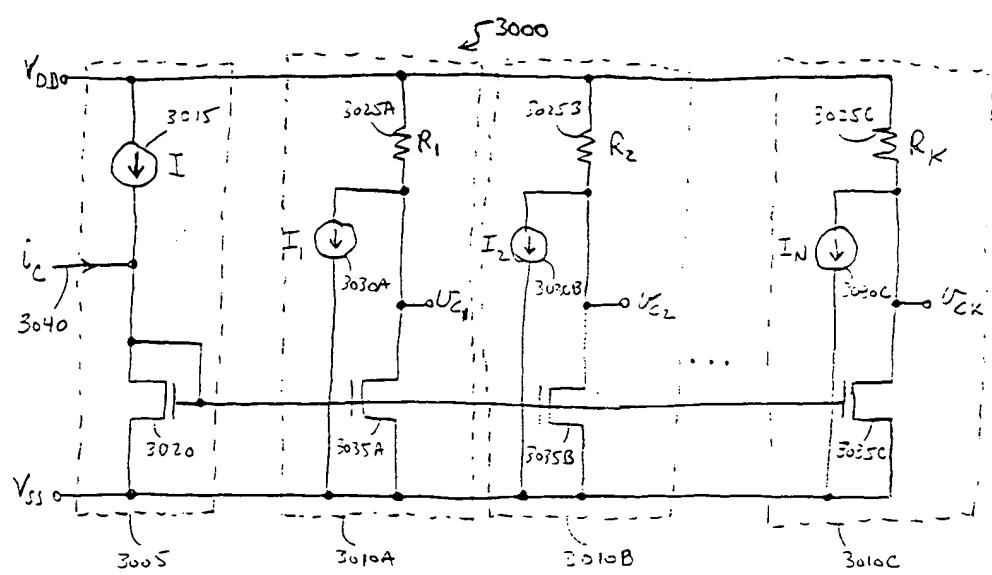


FIG. 30

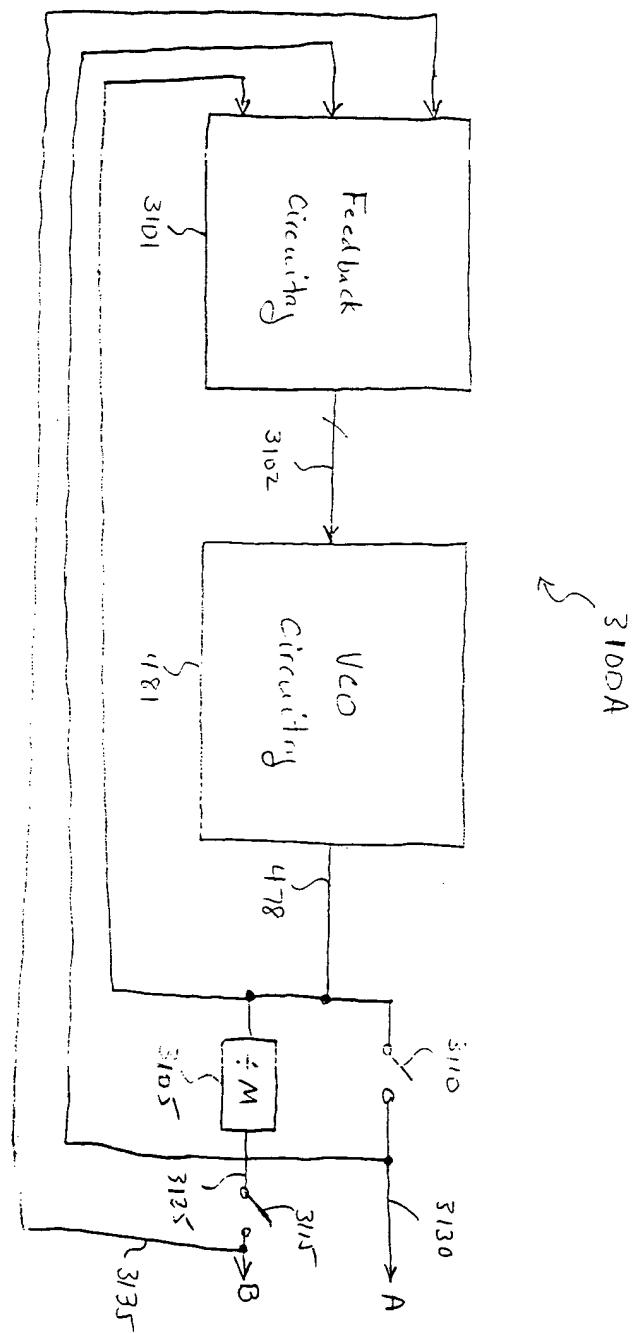


Fig. 31A

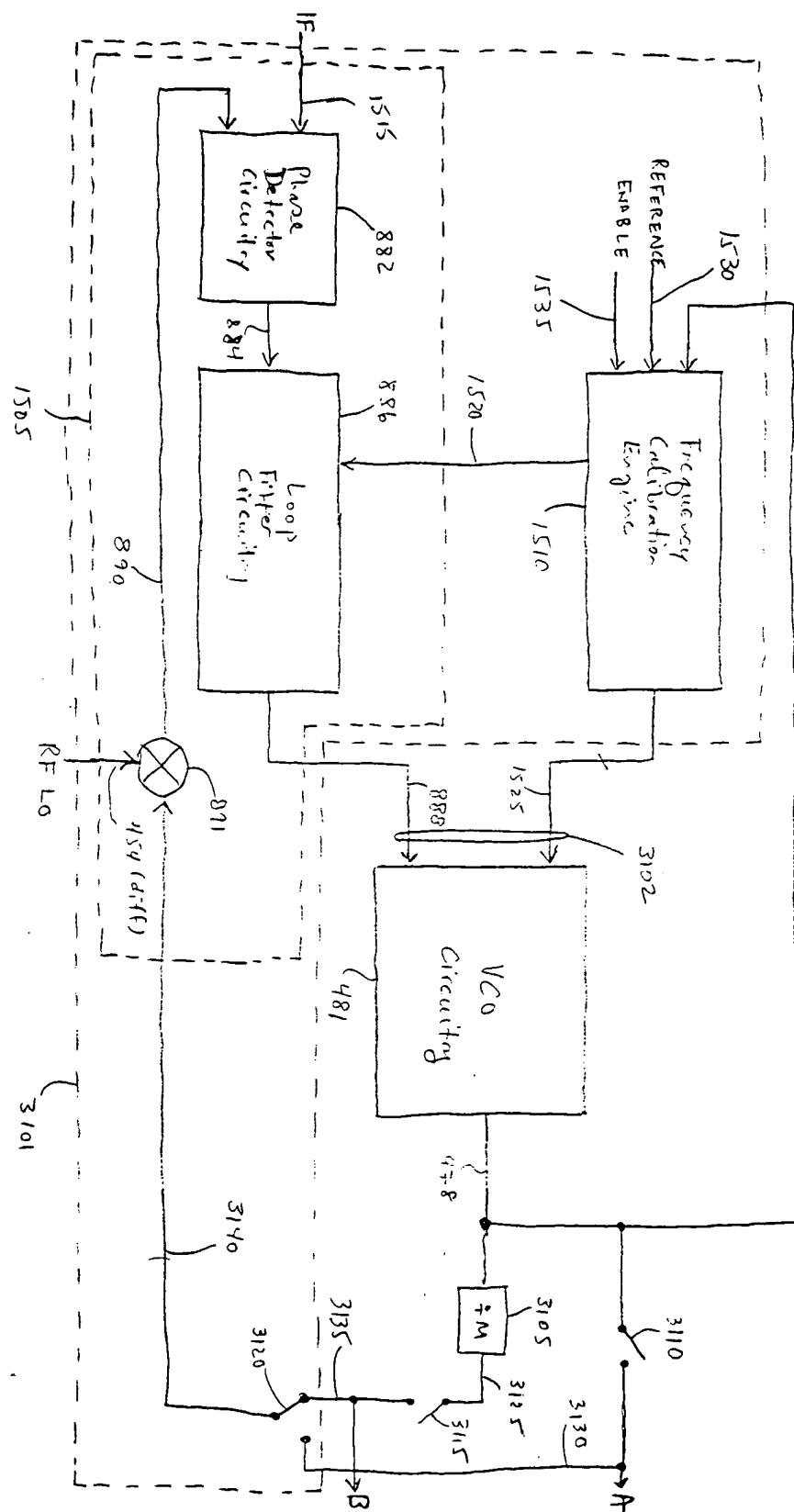


FIG. 31B

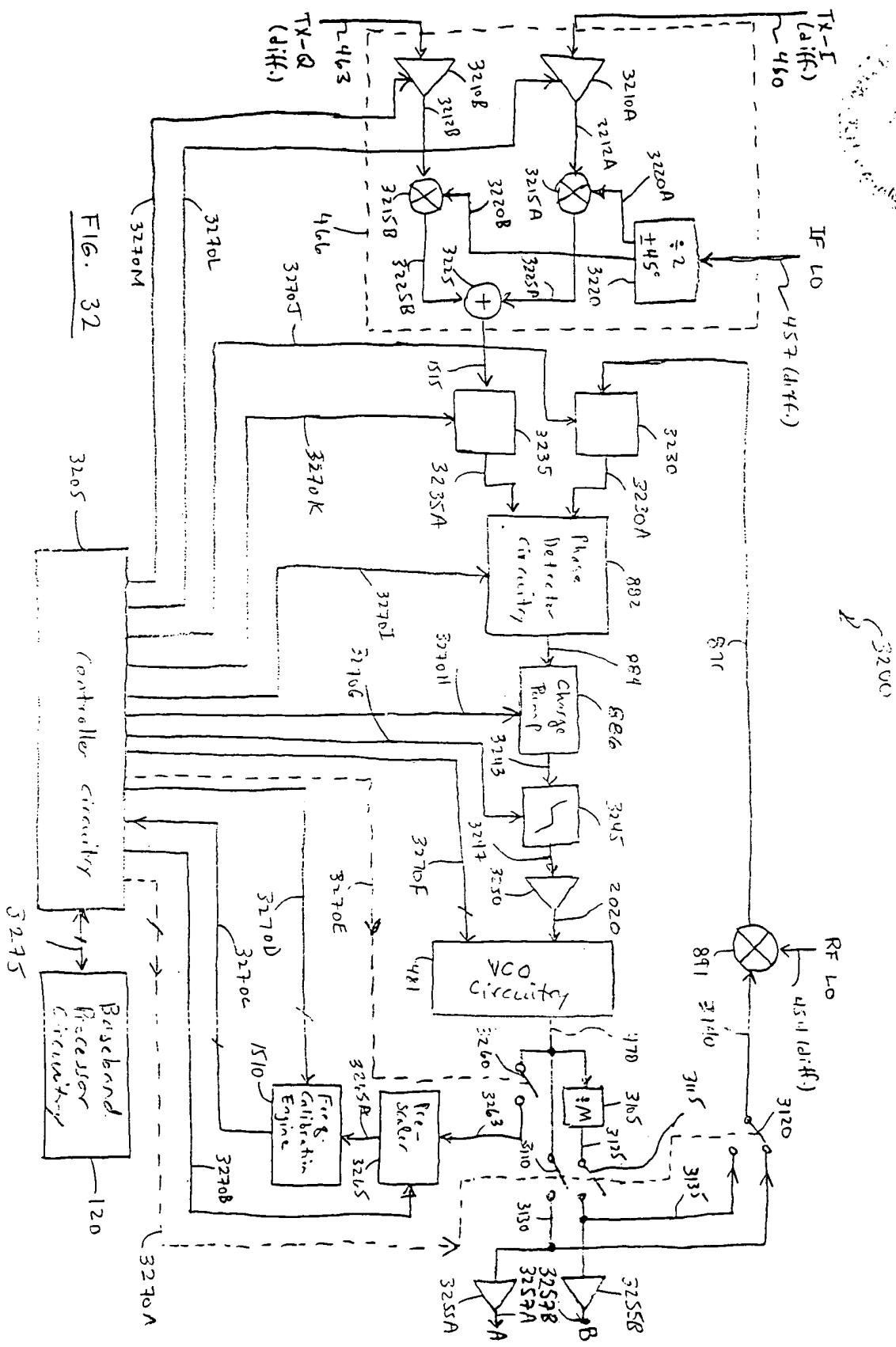


FIG. 32

32VDC

RF LO

IF LO

TX-Q

TX-I

TX

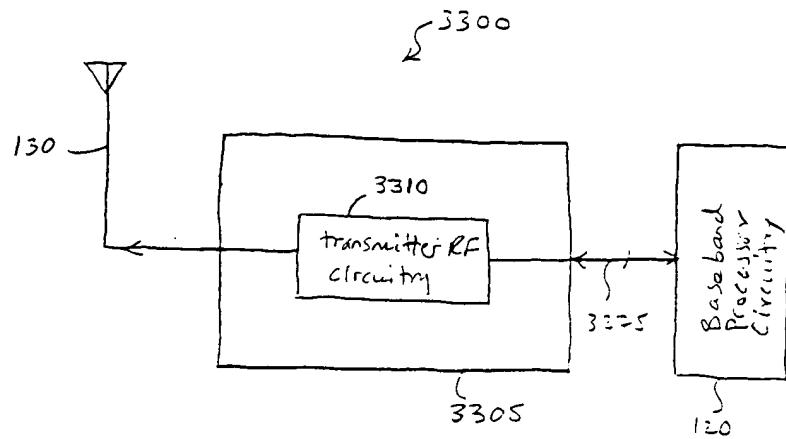


FIG. 33

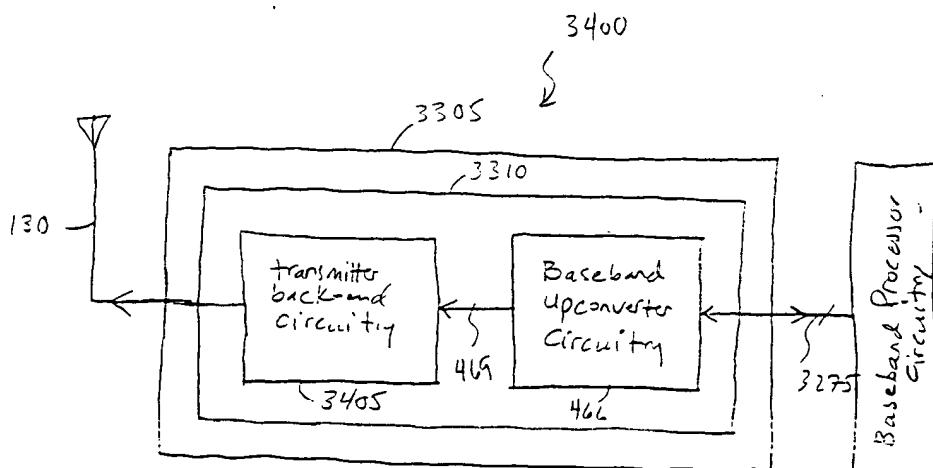


FIG. 34

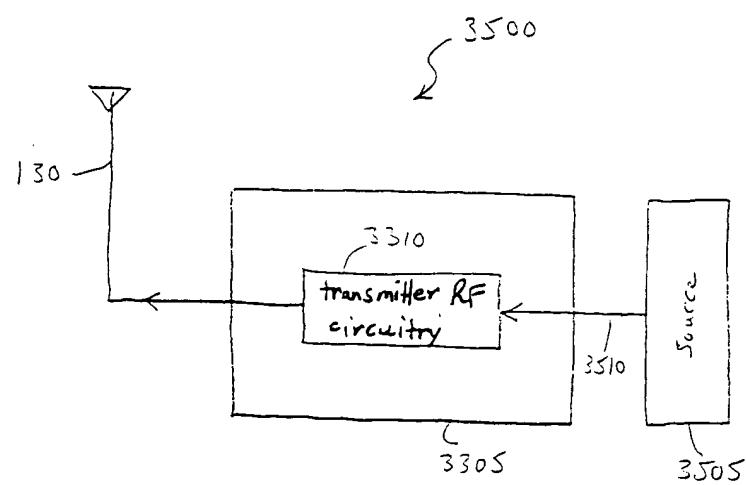


FIG. 35